SPEC® CFP2006 Result

Dell Inc.

(Test Sponsor: Intel Corporation)

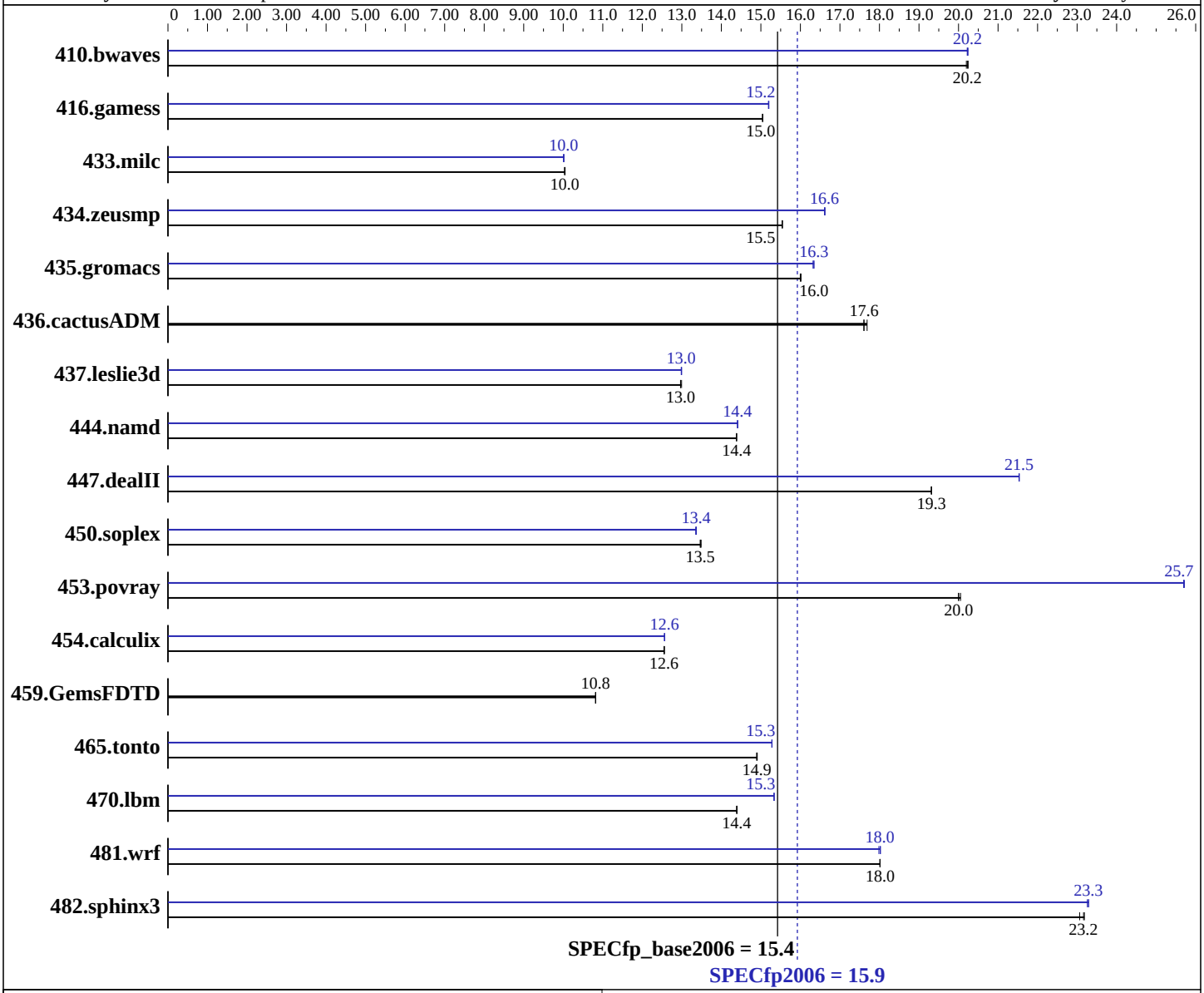
XPS M1730 (Intel Core 2 Extreme X7900)

SPECfp®2006 = 15.9

SPECfp_base2006 = 15.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2007
Hardware Availability: Aug-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core 2 Extreme X7900
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ and Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
Disk Subsystem: 80GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	673	20.2	671	20.2	**672**	**20.2**	**672**	**20.2**	672	20.2	671	20.2
416.gamess	1302	15.0	**1302**	**15.0**	1301	15.0	1289	15.2	**1288**	**15.2**	1288	15.2
433.milc	915	10.0	**915**	**10.0**	914	10.0	918	10.0	**917**	**10.0**	916	10.0
434.zeusmp	585	15.5	586	15.5	**585**	**15.5**	**548**	**16.6**	548	16.6	547	16.6
435.gromacs	446	16.0	**446**	**16.0**	446	16.0	437	16.3	**437**	**16.3**	437	16.3
436.cactusADM	679	17.6	**679**	**17.6**	676	17.7	679	17.6	**679**	**17.6**	676	17.7
437.leslie3d	725	13.0	723	13.0	**725**	**13.0**	724	13.0	723	13.0	**724**	**13.0**
444.namd	557	14.4	**557**	**14.4**	558	14.4	**556**	**14.4**	557	14.4	556	14.4
447.dealII	592	19.3	**592**	**19.3**	592	19.3	531	21.5	531	21.5	**531**	**21.5**
450.soplex	**619**	**13.5**	619	13.5	618	13.5	625	13.4	624	13.4	**624**	**13.4**
453.povray	265	20.1	**266**	**20.0**	266	20.0	207	25.7	**207**	**25.7**	207	25.7
454.calculix	657	12.6	**657**	**12.6**	657	12.6	656	12.6	657	12.6	**657**	**12.6**
459.GemsFDTD	**981**	**10.8**	980	10.8	981	10.8	**981**	**10.8**	980	10.8	981	10.8
465.tonto	660	14.9	**660**	**14.9**	661	14.9	**644**	**15.3**	644	15.3	644	15.3
470.lbm	955	14.4	**955**	**14.4**	954	14.4	896	15.3	**896**	**15.3**	896	15.3
481.wrf	620	18.0	**620**	**18.0**	620	18.0	621	18.0	**620**	**18.0**	620	18.0
482.sphinx3	845	23.1	**841**	**23.2**	841	23.2	837	23.3	838	23.3	**837**	**23.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
nVidia GeForce 8700M GT discrete graphics card
Binaries were built on Windows XP Professional SP2
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000

Benchmarks using both Fortran and C:
-fast /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -fast /F950000000

416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Ob0
-Qansi-alias -Qscalar-rep- /F950000000

434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec_div-
-Qunroll0 -Qscalar-rep- /F950000000

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Peak Optimization Flags (Continued)

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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