SPEC® CFP2006 Result

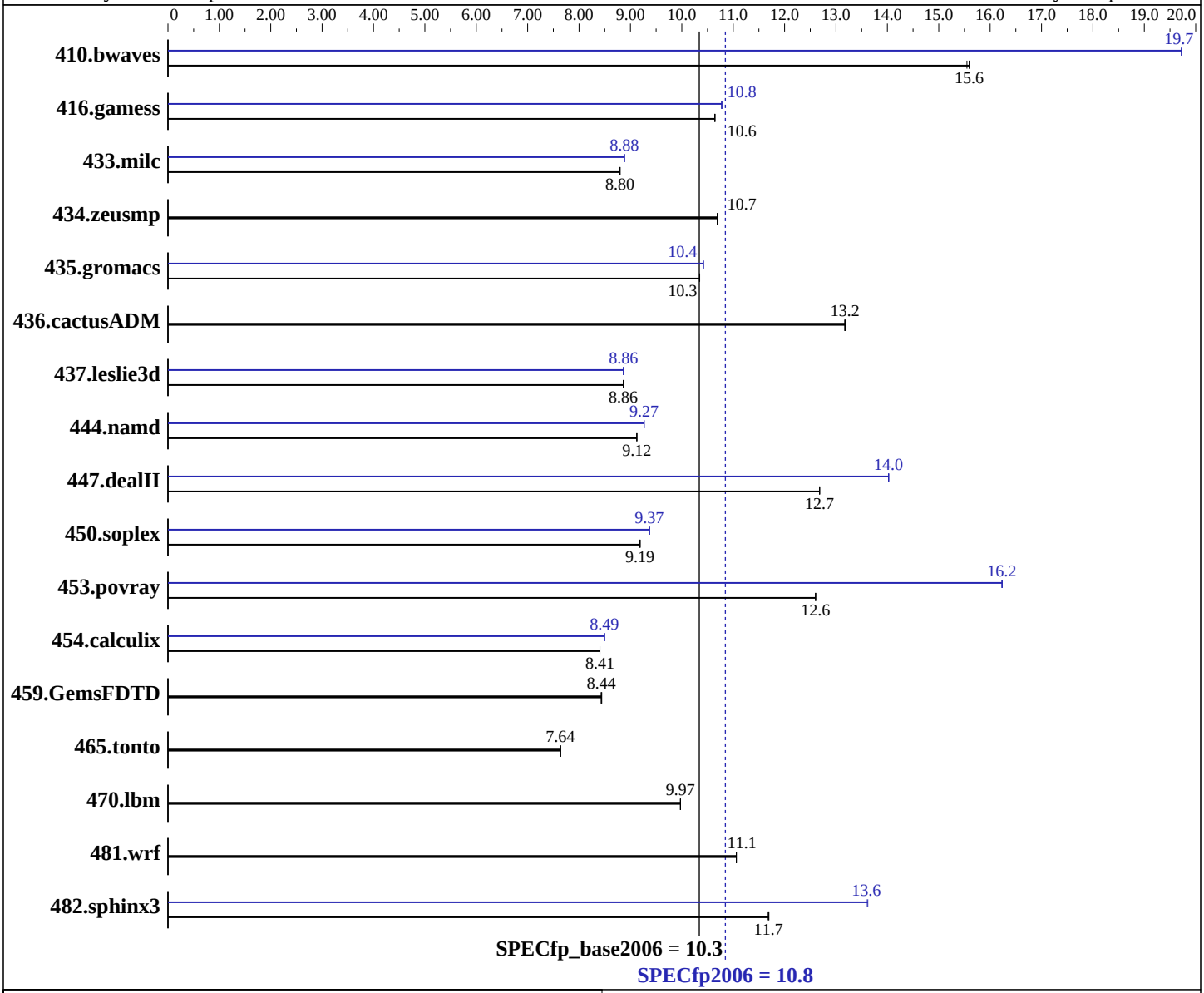
Supermicro
Motherboard PDSBA+

SPECfp®2006 = 10.8

SPECfp_base2006 = 10.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2007
Hardware Availability: Apr-2007
Software Availability: Apr-2007



Hardware

CPU Name:	Intel Core 2 Duo E4300
CPU Characteristics:	1.8GHz, 800MHz bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows XP Professional w/ SP2
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z Intel Fortran Compiler for IA32 version 9.1 Build no 20070322Z
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2X 1GB ECC, CL4, 533MHz, UnBuffer)
Disk Subsystem: 150GB SATA, 7200RPM
Other Hardware: None

Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	874	15.5	**872**	**15.6**	872	15.6	689	19.7	**689**	**19.7**	689	19.7
416.gamess	1839	10.6	1840	10.6	**1840**	**10.6**	1817	10.8	**1817**	**10.8**	1817	10.8
433.milc	1044	8.79	**1043**	**8.80**	1043	8.80	1034	8.88	1033	8.88	**1034**	**8.88**
434.zeusmp	851	10.7	851	10.7	**851**	**10.7**	851	10.7	851	10.7	**851**	**10.7**
435.gromacs	**690**	**10.3**	690	10.3	690	10.3	685	10.4	**685**	**10.4**	685	10.4
436.cactusADM	907	13.2	**907**	**13.2**	907	13.2	907	13.2	**907**	**13.2**	907	13.2
437.leslie3d	**1061**	**8.86**	1061	8.86	1060	8.87	**1060**	**8.86**	1060	8.86	1060	8.87
444.namd	879	9.12	879	9.12	**879**	**9.12**	866	9.27	865	9.27	**866**	**9.27**
447.dealII	**902**	**12.7**	902	12.7	902	12.7	816	14.0	816	14.0	**816**	**14.0**
450.soplex	908	9.18	908	9.19	**908**	**9.19**	890	9.37	891	9.36	**890**	**9.37**
453.povray	422	12.6	**422**	**12.6**	422	12.6	328	16.2	328	16.2	**328**	**16.2**
454.calculix	981	8.41	**981**	**8.41**	981	8.41	**971**	**8.49**	971	8.49	971	8.49
459.GemsFDTD	1257	8.44	1259	8.43	**1258**	**8.44**	1257	8.44	1259	8.43	**1258**	**8.44**
465.tonto	1288	7.64	1288	7.64	**1288**	**7.64**	1288	7.64	1288	7.64	**1288**	**7.64**
470.lbm	1378	9.97	1378	9.97	**1378**	**9.97**	1378	9.97	1378	9.97	**1378**	**9.97**
481.wrf	1010	11.1	1010	11.1	**1010**	**11.1**	1010	11.1	1010	11.1	**1010**	**11.1**
482.sphinx3	1667	11.7	1669	11.7	**1668**	**11.7**	1435	13.6	**1434**	**13.6**	1431	13.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with SC733T-645 case,
To ensure system stability, a 450W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.
Product description located as of http://www.supermicro.com/products/motherboard/Core2Duo/965/PDSBA+.cfm
The system bus runs at 800 MHz
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Peak Optimization Flags

C benchmarks:

433.milc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

470.lbm: `basepeak = yes`

482.sphinx3: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxB -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:

410.bwaves: `-QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000 libguide.lib libguide40.lib -link /FORCE:MULTIPLE`

416.gamess: `Same as 410.bwaves`

434.zeusmp: `basepeak = yes`

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 -link /FORCE:MULTIPLE`

459.GemsFDTD: `basepeak = yes`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `-QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib libguide.lib libguide40.lib -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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