



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp®2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECfp_base2006 = 14.0

CPU2006 license: 11

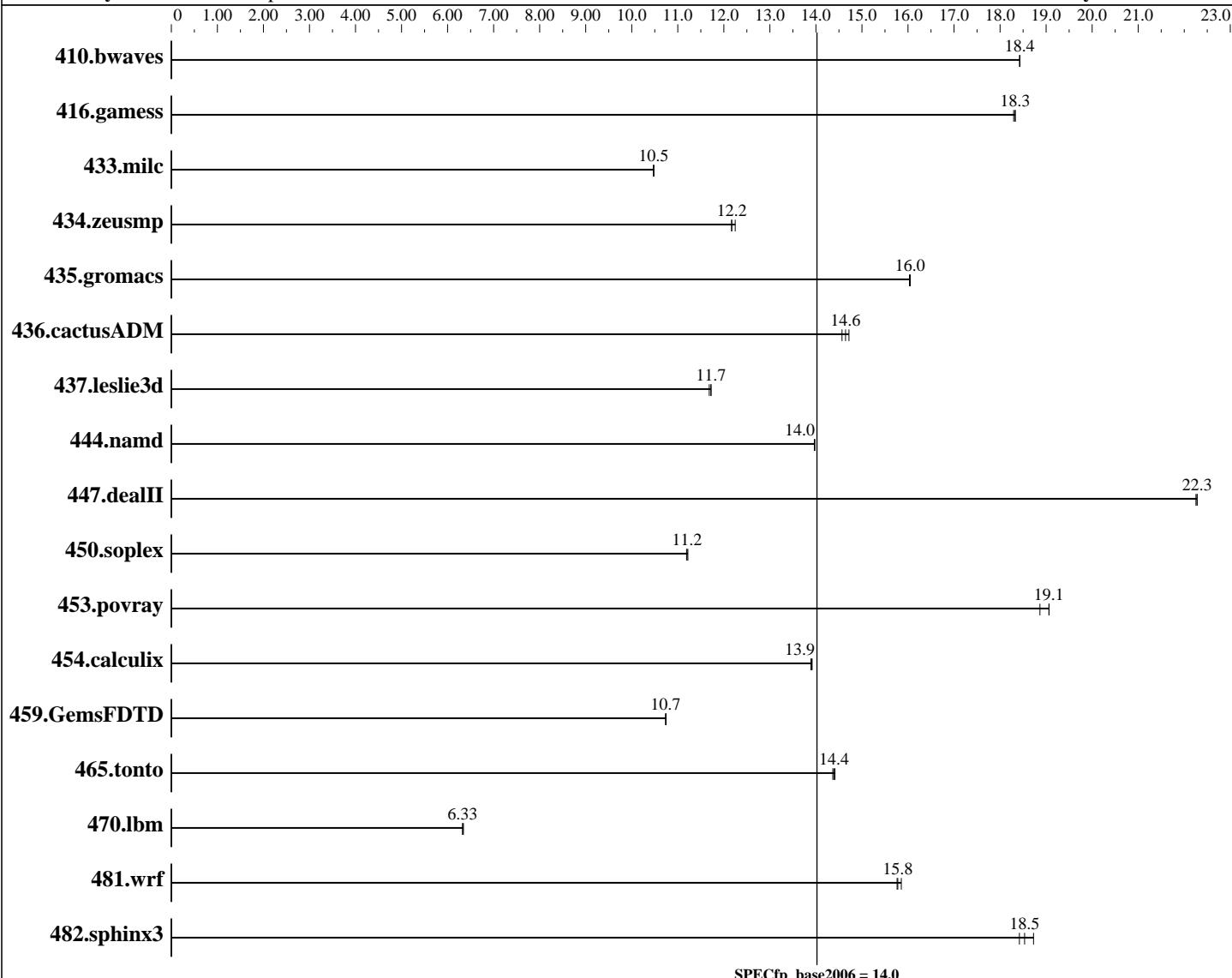
Test date: Apr-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
CPU Characteristics: 1333MHz system bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
Package ID l_cc_c_9.1.045 Build no 20061101
Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel: No

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECfp_base2006 = 14.0

CPU2006 license: 11

Test date: Apr-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Nov-2006

L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 10000 RPM
 Other Hardware: Memory and I/O Expansion Unit (P/N 42C1600)

File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	737	18.4	737	18.4	738	18.4						
416.gamess	1068	18.3	1070	18.3	1068	18.3						
433.milc	876	10.5	877	10.5	876	10.5						
434.zeusmp	748	12.2	743	12.2	748	12.2						
435.gromacs	445	16.0	445	16.0	445	16.0						
436.cactusADM	820	14.6	812	14.7	816	14.6						
437.leslie3d	802	11.7	802	11.7	805	11.7						
444.namd	574	14.0	574	14.0	574	14.0						
447.dealII	513	22.3	514	22.3	514	22.3						
450.soplex	743	11.2	745	11.2	745	11.2						
453.povray	279	19.1	282	18.9	279	19.1						
454.calculix	593	13.9	593	13.9	594	13.9						
459.GemsFDTD	988	10.7	988	10.7	988	10.7						
465.tonto	683	14.4	684	14.4	685	14.4						
470.lbm	2166	6.34	2172	6.33	2171	6.33						
481.wrf	709	15.8	705	15.9	708	15.8						
482.sphinx3	1052	18.5	1041	18.7	1058	18.4						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

"ulimit -s unlimited" set

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 =

Not Run

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECfp_base2006 =

14.0

CPU2006 license: 11

Test date:

Apr-2007

Test sponsor: IBM Corporation

Hardware Availability:

Apr-2007

Tested by: IBM Corporation

Software Availability:

Nov-2006

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/ibm-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/ibm-ic91-flags.xml>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECfp_base2006 = 14.0

CPU2006 license: 11

Test date: Apr-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Nov-2006

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 12:08:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 15 May 2007.