



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp®2006 = 10.9

SPECfp_base2006 = 10.8

CPU2006 license: 3

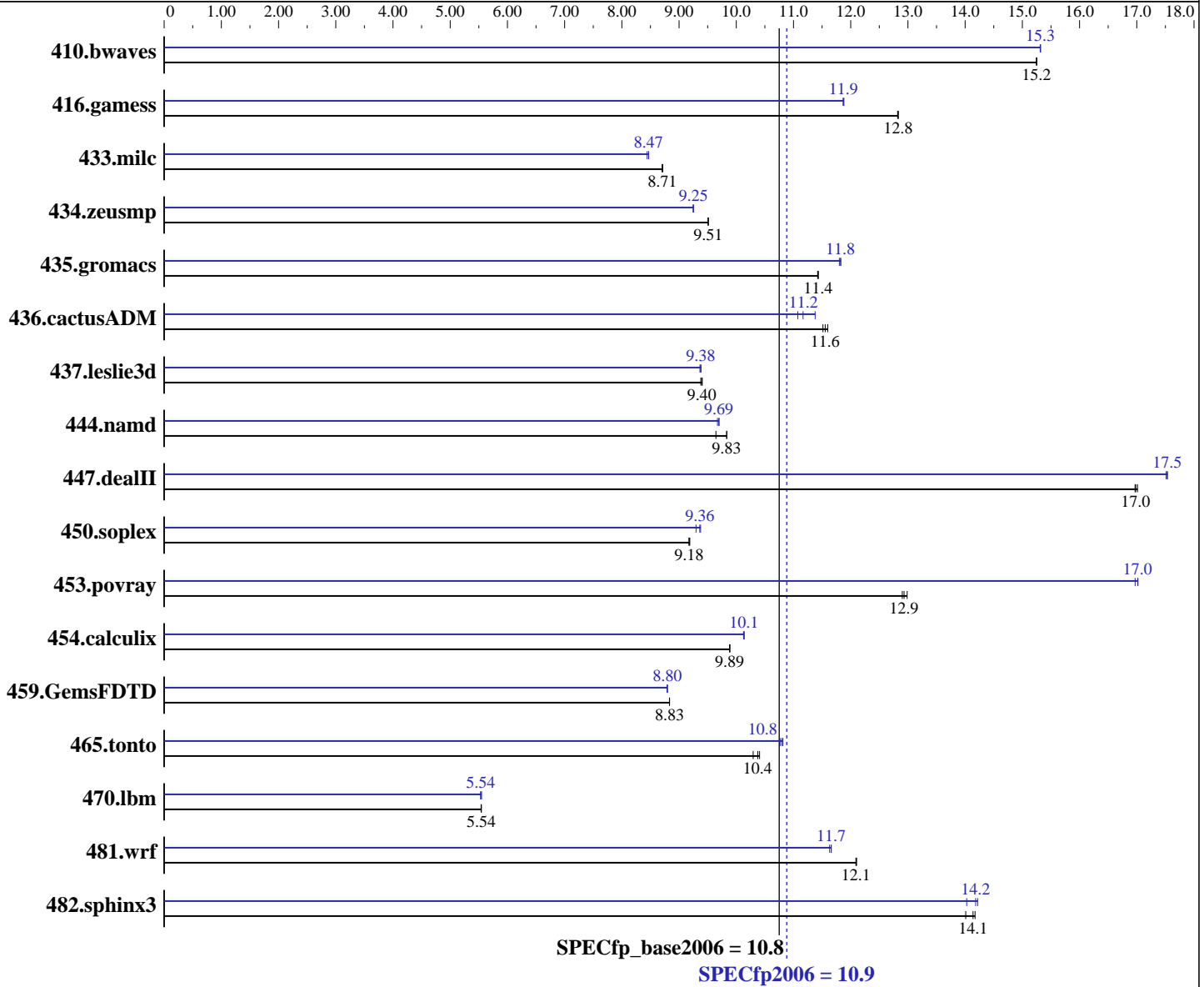
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 4x36 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	891	15.3	891	15.2	891	15.2	887	15.3	888	15.3	887	15.3
416.gamess	1527	12.8	1526	12.8	1526	12.8	1650	11.9	1649	11.9	1648	11.9
433.milc	1054	8.71	1055	8.70	1053	8.71	1088	8.44	1084	8.47	1084	8.47
434.zeusmp	957	9.51	957	9.51	957	9.51	984	9.25	984	9.25	984	9.25
435.gromacs	625	11.4	625	11.4	625	11.4	605	11.8	604	11.8	604	11.8
436.cactusADM	1038	11.5	1034	11.6	1030	11.6	1079	11.1	1070	11.2	1050	11.4
437.leslie3d	1000	9.40	999	9.41	1002	9.38	1002	9.38	1004	9.36	1002	9.38
444.namd	816	9.83	816	9.83	832	9.64	827	9.69	827	9.70	829	9.67
447.dealII	674	17.0	674	17.0	672	17.0	652	17.5	653	17.5	652	17.5
450.soplex	909	9.17	909	9.18	908	9.19	891	9.36	897	9.30	889	9.38
453.povray	411	12.9	412	12.9	410	13.0	313	17.0	313	17.0	313	17.0
454.calculix	834	9.89	835	9.89	835	9.88	814	10.1	815	10.1	814	10.1
459.GemsFDTD	1201	8.83	1201	8.83	1201	8.83	1206	8.80	1207	8.79	1206	8.80
465.tonto	945	10.4	948	10.4	956	10.3	910	10.8	911	10.8	913	10.8
470.lbm	2478	5.55	2479	5.54	2480	5.54	2476	5.55	2484	5.53	2482	5.54
481.wrf	924	12.1	923	12.1	924	12.1	958	11.7	958	11.7	960	11.6
482.sphinx3	1375	14.2	1379	14.1	1391	14.0	1371	14.2	1389	14.0	1374	14.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
"ulimit -s unlimited" set
Single processor kernel used

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>



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For other inquiries, please contact webmaster@spec.org.

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