



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL360 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp®2006 = 10.9

SPECfp_base2006 = 10.8

CPU2006 license: 3

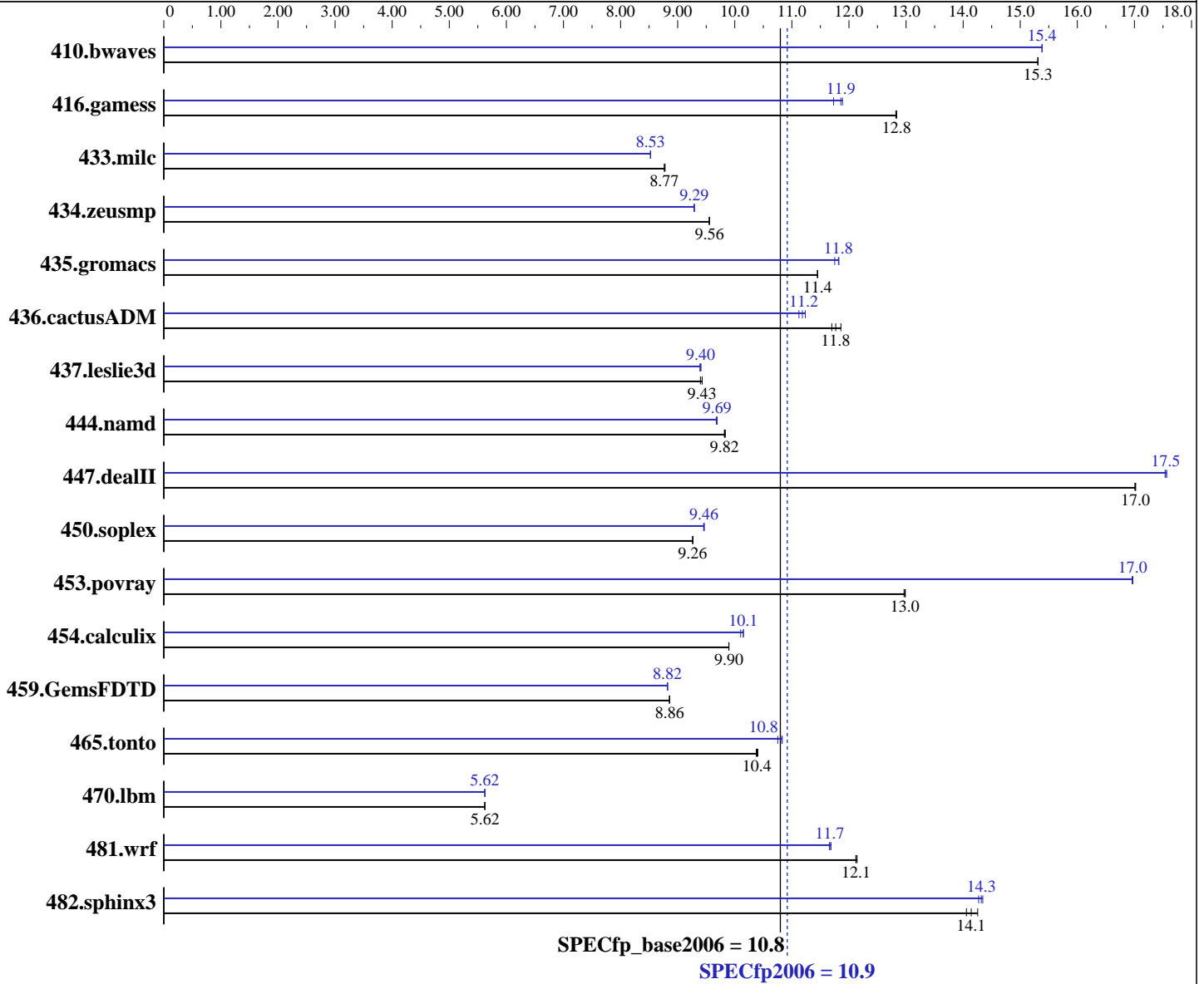
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 2x72 GB 10k SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	888	15.3	887	15.3	888	15.3	883	15.4	884	15.4	883	15.4
416.gamess	1526	12.8	1527	12.8	1526	12.8	1669	11.7	1647	11.9	1651	11.9
433.milc	1047	8.77	1047	8.77	1046	8.78	1076	8.53	1077	8.53	1077	8.52
434.zeusmp	952	9.56	952	9.56	952	9.55	980	9.29	979	9.29	979	9.29
435.gromacs	623	11.5	624	11.4	624	11.4	604	11.8	608	11.8	604	11.8
436.cactusADM	1008	11.9	1015	11.8	1021	11.7	1074	11.1	1064	11.2	1068	11.2
437.leslie3d	997	9.43	997	9.43	1000	9.40	1000	9.40	999	9.41	1001	9.39
444.namd	817	9.82	817	9.82	815	9.84	827	9.69	828	9.69	829	9.68
447.dealII	672	17.0	673	17.0	672	17.0	651	17.6	652	17.5	652	17.5
450.soplex	900	9.27	901	9.26	900	9.26	881	9.47	881	9.46	882	9.45
453.povray	410	13.0	410	13.0	410	13.0	314	17.0	313	17.0	314	17.0
454.calculix	834	9.90	834	9.90	834	9.90	817	10.1	813	10.2	813	10.1
459.GemsFDTD	1198	8.86	1198	8.86	1198	8.86	1202	8.82	1203	8.82	1203	8.82
465.tonto	947	10.4	946	10.4	948	10.4	915	10.8	909	10.8	908	10.8
470.lbm	2443	5.62	2444	5.62	2443	5.62	2443	5.62	2442	5.63	2445	5.62
481.wrf	920	12.1	921	12.1	922	12.1	956	11.7	958	11.7	958	11.7
482.sphinx3	1367	14.3	1378	14.1	1386	14.1	1359	14.3	1361	14.3	1366	14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
Single processor kernel used.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>



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