



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®2006 = 14.4

SPECfp_base2006 = 14.3

CPU2006 license: 3

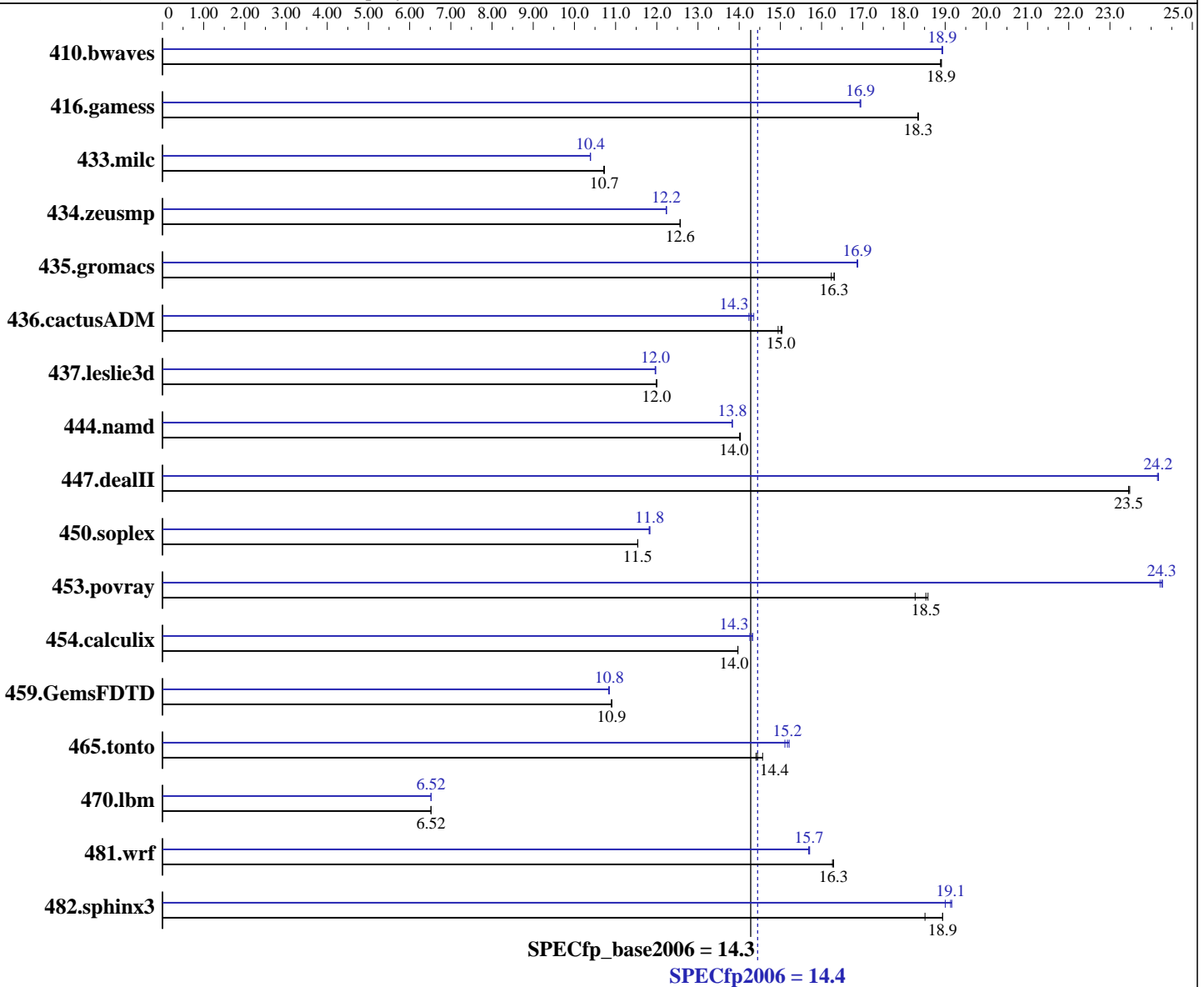
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Package ID l_cc_c_9.1.045 Build no 20061101
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Package ID l_fc_c_9.1.040 Build no 20061101
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem: 2x72 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	720	18.9	719	18.9	<u>719</u>	<u>18.9</u>	<u>718</u>	<u>18.9</u>	718	18.9	718	18.9
416.gamess	1067	18.3	1068	18.3	<u>1067</u>	<u>18.3</u>	<u>1155</u>	<u>16.9</u>	1155	16.9	1156	16.9
433.milc	857	10.7	856	10.7	<u>856</u>	<u>10.7</u>	<u>884</u>	<u>10.4</u>	884	10.4	883	10.4
434.zeusmp	724	12.6	<u>724</u>	<u>12.6</u>	724	12.6	744	12.2	<u>744</u>	<u>12.2</u>	744	12.2
435.gromacs	438	16.3	<u>438</u>	<u>16.3</u>	440	16.2	423	16.9	423	16.9	<u>423</u>	<u>16.9</u>
436.cactusADM	<u>796</u>	<u>15.0</u>	800	14.9	795	15.0	<u>836</u>	<u>14.3</u>	839	14.2	833	14.4
437.leslie3d	784	12.0	783	12.0	<u>784</u>	<u>12.0</u>	785	12.0	<u>785</u>	<u>12.0</u>	786	12.0
444.namd	572	14.0	<u>572</u>	<u>14.0</u>	572	14.0	<u>580</u>	<u>13.8</u>	580	13.8	580	13.8
447.dealII	487	23.5	488	23.5	<u>488</u>	<u>23.5</u>	<u>473</u>	<u>24.2</u>	474	24.2	473	24.2
450.soplex	<u>723</u>	<u>11.5</u>	723	11.5	723	11.5	706	11.8	705	11.8	<u>705</u>	<u>11.8</u>
453.povray	<u>287</u>	<u>18.5</u>	291	18.3	286	18.6	<u>219</u>	<u>24.3</u>	220	24.2	219	24.3
454.calculix	591	14.0	<u>591</u>	<u>14.0</u>	591	14.0	<u>576</u>	<u>14.3</u>	576	14.3	579	14.3
459.GemsFDTD	973	10.9	<u>973</u>	<u>10.9</u>	973	10.9	<u>978</u>	<u>10.8</u>	978	10.8	980	10.8
465.tonto	<u>681</u>	<u>14.4</u>	675	14.6	683	14.4	647	15.2	651	15.1	<u>649</u>	<u>15.2</u>
470.lbm	2107	6.52	<u>2108</u>	<u>6.52</u>	2108	6.52	2106	6.52	<u>2108</u>	<u>6.52</u>	2108	6.52
481.wrf	<u>686</u>	<u>16.3</u>	686	16.3	687	16.3	711	15.7	<u>712</u>	<u>15.7</u>	712	15.7
482.sphinx3	<u>1029</u>	<u>18.9</u>	1053	18.5	1029	18.9	<u>1019</u>	<u>19.1</u>	1017	19.2	1026	19.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
Single processor kernel used

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:

-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>



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For other inquiries, please contact webmaster@spec.org.

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