



SPEC® CFP2006 Result

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Hewlett-Packard Company

HP Integrity BL860c
(1.6GHz/18MB Dual-Core Intel Itanium 2)

SPECfp®2006 = 17.7

SPECfp_base2006 = 16.9

CPU2006 license: 03

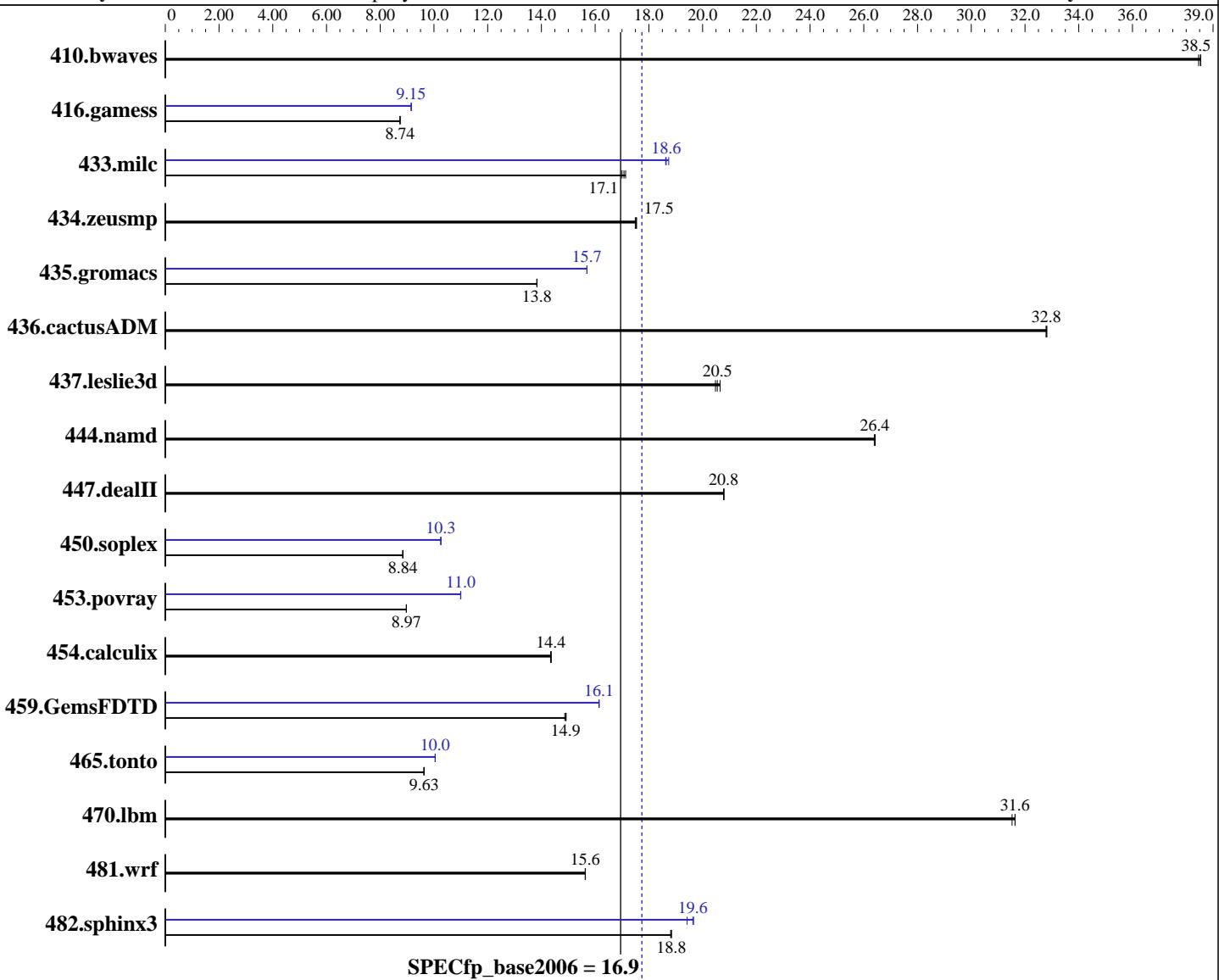
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Feb-2007



Hardware

CPU Name: Dual-Core Intel Itanium 2 9040
CPU Characteristics: 1.6GHz/18MB, 533MHz FSB
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 16 KB I + 16 KB D on chip per core
Secondary Cache: 1 MB I + 256 KB D on chip per core

Software

Operating System: HPUX11i-TCOE B.11.23.0609
Compiler: HP C/aC++ Developer's Bundle C.11.23.12
HP Fortran90 Compiler B.11.23.32
Auto Parallel: No
File System: vxfs
System State: Multi-user
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None

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L3 Cache: 9 MB I+D on chip per core
Other Cache: None
Memory: 12 GB (12x1GB DIMMs)
Disk Subsystem: 73GB 10K RPM SAS
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	353	38.5	353	38.5	<u>353</u>	<u>38.5</u>	353	38.5	353	38.5	<u>353</u>	<u>38.5</u>
416.gamess	2241	8.74	<u>2241</u>	<u>8.74</u>	2241	8.74	2138	9.16	<u>2139</u>	<u>9.15</u>	2140	9.15
433.milc	536	17.1	540	17.0	<u>538</u>	<u>17.1</u>	490	18.7	<u>492</u>	<u>18.6</u>	493	18.6
434.zeusmp	519	17.5	520	17.5	<u>519</u>	<u>17.5</u>	519	17.5	520	17.5	<u>519</u>	<u>17.5</u>
435.gromacs	516	13.8	<u>516</u>	<u>13.8</u>	516	13.8	<u>455</u>	<u>15.7</u>	455	15.7	455	15.7
436.cactusADM	364	32.8	<u>364</u>	<u>32.8</u>	364	32.8	364	32.8	<u>364</u>	<u>32.8</u>	364	32.8
437.leslie3d	455	20.7	459	20.5	<u>457</u>	<u>20.5</u>	455	20.7	459	20.5	<u>457</u>	<u>20.5</u>
444.namd	304	26.4	304	26.4	<u>304</u>	<u>26.4</u>	304	26.4	304	26.4	<u>304</u>	<u>26.4</u>
447.dealII	550	20.8	550	20.8	<u>550</u>	<u>20.8</u>	550	20.8	550	20.8	<u>550</u>	<u>20.8</u>
450.soplex	942	8.85	944	8.84	<u>943</u>	<u>8.84</u>	813	10.3	<u>813</u>	<u>10.3</u>	813	10.3
453.povray	<u>593</u>	<u>8.97</u>	593	8.97	593	8.97	484	11.0	484	11.0	<u>484</u>	<u>11.0</u>
454.calculix	<u>575</u>	<u>14.4</u>	575	14.3	574	14.4	<u>575</u>	<u>14.4</u>	575	14.3	574	14.4
459.GemsFDTD	711	14.9	714	14.9	<u>712</u>	<u>14.9</u>	<u>657</u>	<u>16.1</u>	657	16.1	657	16.1
465.tonto	1022	9.63	<u>1022</u>	<u>9.63</u>	1022	9.63	980	10.0	979	10.0	<u>979</u>	<u>10.0</u>
470.lbm	<u>434</u>	<u>31.6</u>	434	31.6	436	31.5	<u>434</u>	<u>31.6</u>	434	31.6	436	31.5
481.wrf	715	15.6	<u>715</u>	<u>15.6</u>	714	15.6	<u>715</u>	<u>15.6</u>	<u>715</u>	<u>15.6</u>	714	15.6
482.sphinx3	1035	18.8	<u>1035</u>	<u>18.8</u>	1036	18.8	<u>993</u>	<u>19.6</u>	1003	19.4	991	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

The system had the September 2006 HP-UX 11i v2 Technical Computing Operating Environment (TCOE) and compilers installed, along with the following patches:

PHSS_34858 linker + fdp cumulative patch
 PHSS_34853 Math Library Cumulative Patch
 PHSS_34854 Integrity Unwind Library
 PHSS_34855 HP C Compiler (A.06.12)
 PHSS_34856 aC++ Compiler (A.06.12)
 PHSS_34857 u2comp/be/plugin library patch
 PHSS_34395 FORTRAN I/O Library [libI077]
 PHSS_34397 FORTRAN Intrinsics [libF90 B.11.23.17]
 PHSS_34399 Fortran Product Patch, v3.1 to v3.1.1
 PHKL_34020 Perfmon enhancements and Itanium Dual-Core

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Operating System Notes (Continued)

The following kernel tunables were set, in addition to the defaults set by the Technical Computing OE:

```
dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

Platform Notes

The "cpuconfig" EFI command was used prior to booting to deconfigure processors.

Base Compiler Invocation

C benchmarks:

```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:

```
/opt/aCC/bin/aCC -Aa
```

Fortran benchmarks:

```
/opt/fortran90/bin/f90
```

Benchmarks using both Fortran and C:

```
/opt/ansic/bin/cc -Ae /opt/fortran90/bin/f90
```

Base Portability Flags

453.povray: -DSPEC_CPU_NEED_INHVYP

454.calculix: -DSPEC_CPU_NOZMODIFIER

481.wrf: -DNOUNDERSCORE +noppu

Base Optimization Flags

C benchmarks:

```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

C++ benchmarks:

```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
+Ofaster -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N
```

Benchmarks using both Fortran and C:

```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M  
-Wl,+pi,64M -Wl,-N
```

Peak Compiler Invocation

C benchmarks:

```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:

```
/opt/aCC/bin/aCC -Aa
```

Fortran benchmarks:

```
/opt/fortran90/bin/f90
```

Benchmarks using both Fortran and C:

```
/opt/ansic/bin/cc -Ae /opt/fortran90/bin/f90
```

Peak Portability Flags

453.povray: -DSPEC_CPU_NEED_INVHYP

454.calculix: -DSPEC_CPU_NOZMODIFIER

481.wrf: -DNOUNDERSCORE +noppu

Peak Optimization Flags

C benchmarks:

```
433.milc: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M  
-Wl,+pi,64M +Onoparmsoverlap -Wl,-N
```

470.lbm: basepeak = yes

```
482.sphinx3: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M  
-Wl,+pi,64M +Onoparmsoverlap
```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster
+Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M +Onoparmsoverlap -Wl,-N

453.povray: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster
+Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: +Ofaster -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M
+Odataprefetch=direct -Wl,-N

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster
-Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M
+Odataprefetch=direct -Wl,-N

465.tonto: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster
-Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M
+Odataprefetch=direct

Benchmarks using both Fortran and C:

435.gromacs: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster
+Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M +Onoparmsoverlap

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.xml



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