



SPEC[®] CFP2006 Result

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Intel Corporation

Intel DG965WH motherboard
(2.93 GHz, Intel Core 2 Extreme processor
X6800)

SPECfp[®]2006 = **Not Run**

SPECfp_base2006 = **16.8**

CPU2006 license: 13

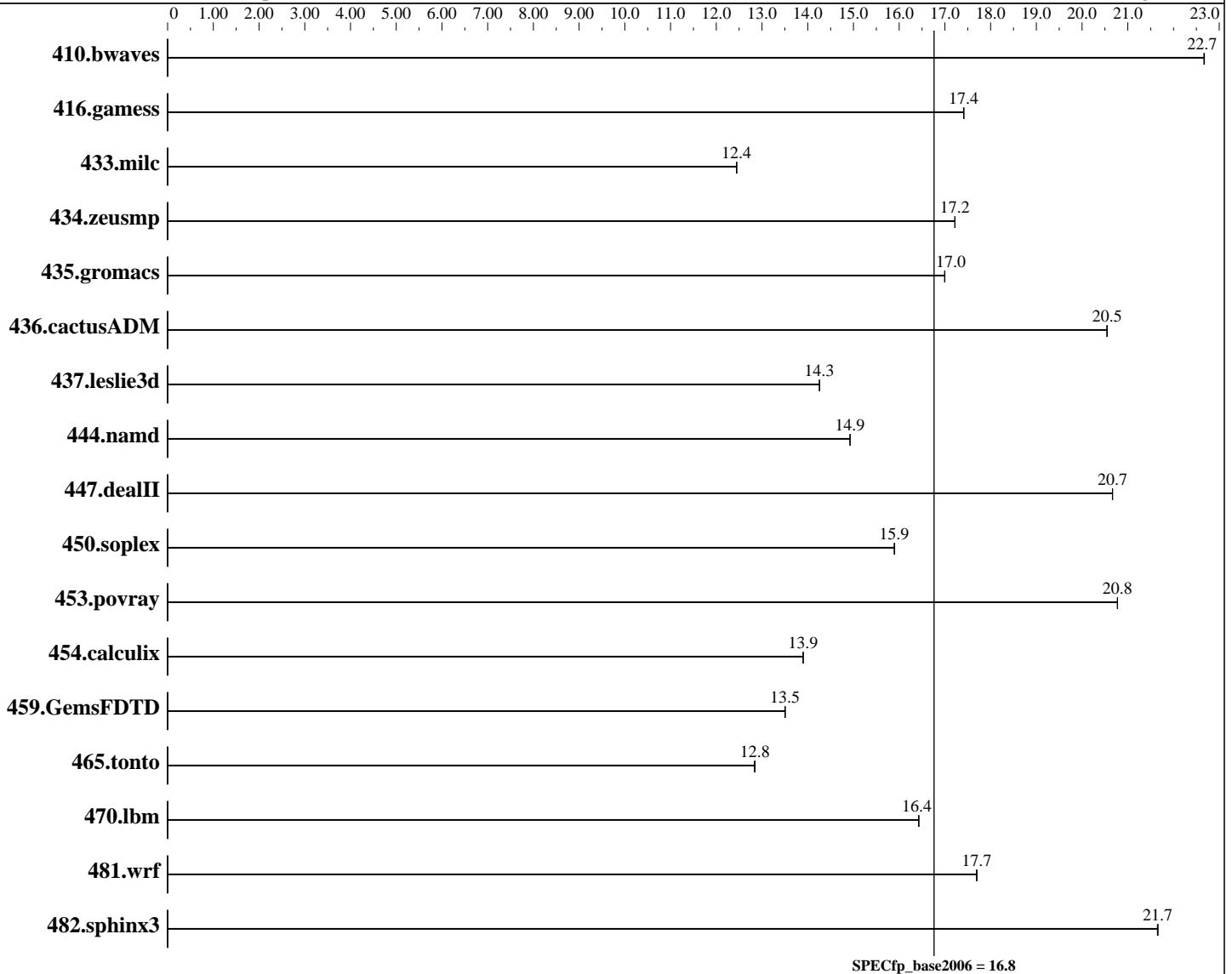
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2006

Hardware Availability: Jul-2006

Software Availability: Aug-2006



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Hardware

CPU Name: Intel Core 2 Extreme X6800
 CPU Characteristics: 2.93 GHz, 1066 MHz bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: Windows XP Professional SP2 32-bit
 Compiler: Intel C++ and Fortran Compiler 9.1 for IA32
 Build 20060816Z
 Microsoft Visual Studio .Net 2003 (for libraries)
 SmartHeap Library Version 8.0 from
<http://www.microquill.com/>
 Auto Parallel: No
 File System: NTFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2 1GB Corsair TWIN2X2048-8500C5 DDR2 1066 CL5-5-5)
Disk Subsystem: Maxtor DiamondMax 10 6B300S0 300GB NCRQ Serial ATA (7200 RPM, 16MB cache)
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	600	22.7	599	22.7	600	22.7						
416.gamess	1124	17.4	1125	17.4	1124	17.4						
433.milc	738	12.4	738	12.4	737	12.5						
434.zeusmp	528	17.2	529	17.2	528	17.2						
435.gromacs	420	17.0	420	17.0	420	17.0						
436.cactusADM	581	20.6	582	20.5	582	20.5						
437.leslie3d	659	14.3	659	14.3	659	14.3						
444.namd	537	14.9	537	14.9	537	14.9						
447.dealII	554	20.7	554	20.7	554	20.7						
450.soplex	524	15.9	525	15.9	525	15.9						
453.povray	256	20.8	256	20.8	256	20.8						
454.calculix	594	13.9	593	13.9	594	13.9						
459.GemsFDTD	786	13.5	786	13.5	785	13.5						
465.tonto	766	12.8	767	12.8	766	12.8						
470.lbm	836	16.4	837	16.4	836	16.4						
481.wrf	631	17.7	631	17.7	631	17.7						
482.sphinx3	900	21.7	900	21.6	900	21.7						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Memory timings set manually to DDR2-800 5-5-5-15 in the bios

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T

453.povray: -DSPEC_CPU_WINDOWS_ICL

454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.xml>



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