



CINT2000 Result

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Itautec

Servidor Itautec LX211 (Intel Xeon processor E5335, 2.00 GHz)

SPECint_rate2000 = 159

SPECint_rate_base2000 = 159

SPEC license #: 9001 | Tested by: Itautec | Test date: Feb-2007 | Hardware Avail: Feb-2007 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	8	117	111	8	117	111
175.vpr	8	112	116	8	112	116
176.gcc	8	54.8	186	8	54.8	186
181.mcf	8	133	126	8	133	126
186.crafty	8	54.5	170	8	54.5	170
197.parser	8	120	139	8	120	139
252.eon	8	50.3	240	8	50.3	240
253.perlbnk	8	80.3	208	8	80.3	208
254.gap	8	96.6	106	8	96.6	106
255.vortex	8	64.0	276	8	64.0	276
256.bzip2	8	116	120	8	116	120
300.twolf	8	132	211	8	132	211

Hardware

CPU: Intel Xeon processor E5335 (2.00 GHz, 2x4MB L2, 1333 MHz system bus)
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 6x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: 1 x 120GB SATA 7200RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060519Z (for 32-bit applications),
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Microquill SmartHeap Library v.8.0 for SMP
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

Basepeak=yes is used for all programs
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO sh1SMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO sh1SMPMt.lib

EXTRA LIBRARIES

MicroQuill SmartHeap Library v.8.0 for SMP
<http://www.microquill.com>