



CFP2000 Result

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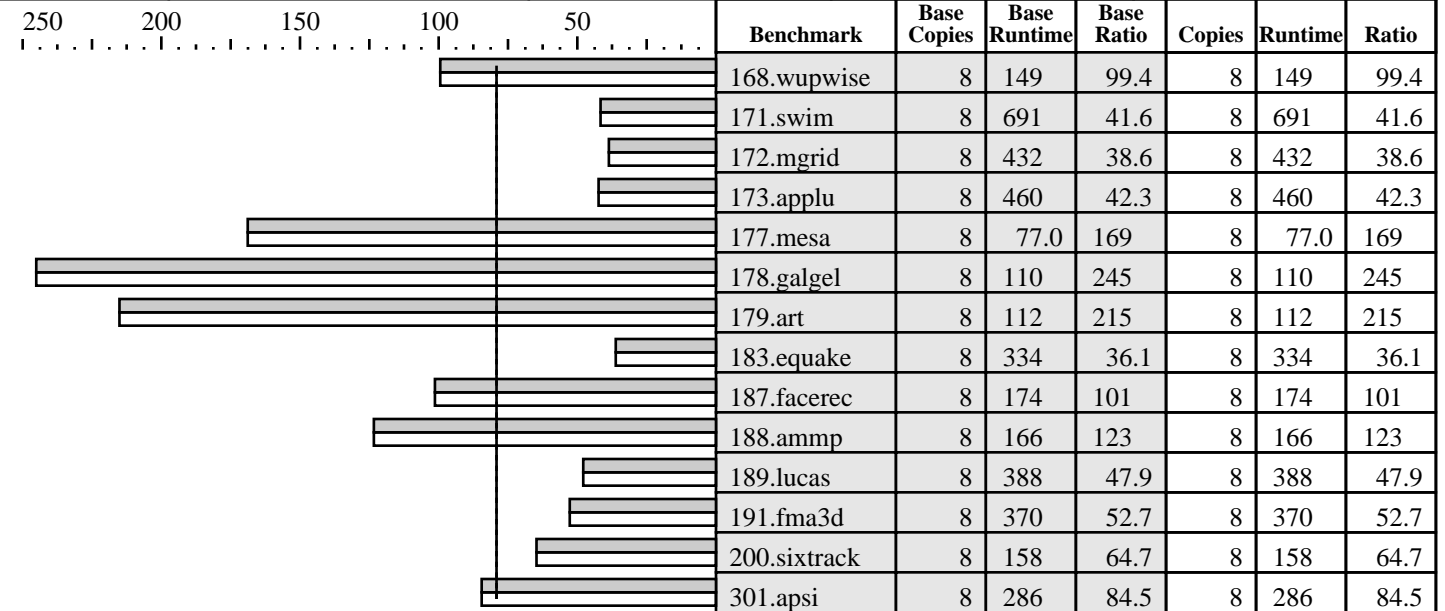
Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5320, 1.86 GHz

SPECfp_rate2000 = 79.1

SPECfp_rate_base2000 = 79.1

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006



Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Parallel: No
 Primary Cache: 32KB(I) + 32KB(D) on chip, per core
 Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
 Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)
 Other Hardware: none

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10
 Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ and Fortran Compiler 9.1 for EM64T
 Build 20060816 (for 64-bit applications)
 File System: reiserfs
 System State: Multi-user run level 3

Notes/Tuning Information

GENERAL

+FDO implies feedback-directed optimization
 PASS1: -prof_gen PASS2: -prof_use

Optimization flags
 ONESTEP=yes set for all benchmarks

Portability flags
 -DSPEC_CPU2000_LP64 applied to all benchmarks
 178.galgel: -FI for fixed-format Fortran

Base tuning flags
 for Fortran and C programs: -fast +FDO

Peak tuning flags
 same as baseline (basepeak=true set globally)



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Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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