



# CINT2000 Result

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## Supermicro

Motherboard X7DBR-8( 2.66GHz, Intel Xeon X5355 processor)

SPECint2000 = 2750

SPECint\_base2000 = 2744

SPEC license #01176 | Tested by: Supermicro | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Mar-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	2000 4000 6000 8000			
164.gzip	1400	84.3	1660	84.0	1667	[Bar chart showing ratio]			
175.vpr	1400	70.3	1993	68.5	2045	[Bar chart showing ratio]			
176.gcc	1100	39.3	2796	39.3	2796	[Bar chart showing ratio]			
181.mcf	1800	42.2	4262	42.2	4262	[Bar chart showing ratio]			
186.crafty	1000	40.7	2460	40.7	2459	[Bar chart showing ratio]			
197.parser	1800	87.9	2047	88.0	2045	[Bar chart showing ratio]			
252.eon	1300	37.4	3478	37.3	3486	[Bar chart showing ratio]			
253.perlbmk	1800	54.9	3280	55.0	3272	[Bar chart showing ratio]			
254.gap	1100	40.3	2729	40.3	2729	[Bar chart showing ratio]			
255.vortex	1900	41.5	4578	41.5	4578	[Bar chart showing ratio]			
256.bzip2	1500	71.1	2110	71.3	2103	[Bar chart showing ratio]			
300.twolf	3000	98.2	3056	98.2	3056	[Bar chart showing ratio]			

### Hardware

CPU: Intel Xeon X5355 processor ( 2.66 GHz,1333MHz bus)  
CPU MHz: 2660  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Parallel: N/A  
Primary Cache: 32KBI + 32KBD on chip per core  
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 X 2GB DDR2 ECC CL5 667MHz FBDIMM  
Disk Subsystem: 1 X Seagate Barracuda 120GB 7200 RPM  
Other Hardware: N/A

### Software

Operating System: Windows Server 2003 Enterprise Edition 32-bits w/ SP1  
Compiler: Intel C++ Compiler 9.1 for 32-bit applications  
Build 20060323Z Package ID: W\_CC\_P\_9.1.020  
Microsoft Visual Studio 2005(for libraries)  
SmartHeap Library Version 8.0 from <http://www.microquill.com/>  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: -DHAS_ERRLIST
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast -Qansi_alias -Oa +FDO
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: -fast -Qansi_alias -Oa +FDO
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbmk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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## Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll11 +FDO  
300.twolf: -fast -O3 +FDO shlw32m.lib

Tested systems can be used with SC816S-R700 case,  
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]  
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBR-8.cfm>

The system bus runs at 1333 MHz

shlw32m-80.lib is the V8.0 SmartHeap library renamed to co-exist with older versions