



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL140 G3 (3.0 GHz, Intel Xeon processor 5160)

SPECint_rate2000 = 122
SPECint_rate_base2000 = 122

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Nov-2006 Hardware Avail: Jun-2006 Software Avail: Oct-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	77.5	83.9	4	77.3	84.1
175.vpr	4	79.8	81.4	4	77.3	84.1
176.gcc	4	40.6	126	4	40.6	126
181.mcf	4	78.5	106	4	78.0	107
186.crafty	4	36.5	127	4	36.5	127
197.parser	4	83.7	99.7	4	83.3	100
252.eon	4	32.9	183	4	33.0	183
253.perlbnk	4	52.0	161	4	52.2	160
254.gap	4	45.4	112	4	45.6	112
255.vortex	4	45.8	192	4	45.8	192
256.bzip2	4	78.3	88.9	4	78.3	88.9
300.twolf	4	86.4	161	4	86.4	161

Hardware

CPU: Intel Xeon processor 5160 (1333 MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8 x 1 GB PC2-5300F)
Disk Subsystem: 2 x 36 GB 15K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (version 9.1 Build 20060816Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Adjacent Cache Line Prefetch Disabled