



CINT2000 Result

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Dell

PowerEdge 2950 (Intel Xeon processor E5345, 2.33 GHz)

SPECint_rate2000 = 185

SPECint_rate_base2000 = 185

SPEC license #: 55 | Tested by: Dell, Inc., Round Rock, TX | Test date: Nov-2006 | Hardware Avail: Dec-2006 | Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 8 | 101 | 129 | 8 | 101 | 128 |
| 175.vpr | 8 | 98.2 | 132 | 8 | 96.9 | 134 |
| 176.gcc | 8 | 48.2 | 212 | 8 | 48.2 | 212 |
| 181.mcf | 8 | 125 | 133 | 8 | 125 | 133 |
| 186.crafty | 8 | 47.1 | 197 | 8 | 47.1 | 197 |
| 197.parser | 8 | 105 | 159 | 8 | 105 | 159 |
| 252.eon | 8 | 42.7 | 283 | 8 | 42.7 | 283 |
| 253.perlbnk | 8 | 65.5 | 255 | 8 | 65.5 | 255 |
| 254.gap | 8 | 74.2 | 138 | 8 | 74.0 | 138 |
| 255.vortex | 8 | 56.3 | 313 | 8 | 56.3 | 313 |
| 256.bzip2 | 8 | 99.8 | 140 | 8 | 102 | 136 |
| 300.twolf | 8 | 113 | 247 | 8 | 113 | 247 |

Hardware

CPU: Quad-Core Intel Xeon processor E5345 (1333 MHz system bus)
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8 MB I+D on chip, per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8 x 1 GB 667 MHz CL5 FB-DIMM DDR2 SDRAM)
Disk Subsystem: 1 x 80GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler: Intel C++ Compiler 9.1 for IA32(20060519Z)
Microsoft Visual Studio .NET 2003(7.1.3088)
MicroQuill SmartHeap Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

ONESTEP=yes
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

BASE TUNING

C: -fast +FDO shlw32M.lib
C++: -fast -Qcxx_features +FDO

PEAK TUNING

164.gzip: -fast +FDO
175.vpr: -fast +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: -fast -Oa +FDO shlw32M.lib
197.parser: -fast +FDO
252.eon: -fast +FDO
253.perlbnk: basepeak=yes



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Notes/Tuning Information (Continued)

```
254.gap:      -fast +FDO
255.vortex    basepeak=yes
256.bzip2:    -fast -Qunroll11 -Oa +FDO
300.twolf:    basepeak=yes
BIOS SETTINGS
  Adjacent Cache Line Prefetch disabled in BIOS
```