



CINT2000 Result

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Itautec

Servidor Itautec LX211 (Intel Xeon 5160 processor)

SPECint_rate2000 = 112

SPECint_rate_base2000 = 112

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	4	78.8	82.5	4	78.8	82.5
175.vpr	4	81.6	79.6	4	81.6	79.6
176.gcc	4	50.6	101	4	50.6	101
181.mcf	4	106	78.8	4	106	78.8
186.crafy	4	36.0	129	4	36.0	129
197.parser	4	88.2	94.7	4	88.2	94.7
252.eon	4	33.3	181	4	33.3	181
253.perlbnk	4	55.5	150	4	55.5	150
254.gap	4	57.7	88.4	4	57.7	88.4
255.vortex	4	45.4	194	4	45.4	194
256.bzip2	4	82.0	84.8	4	82.0	84.8
300.twolf	4	87.6	159	4	87.6	159

Hardware

CPU: Intel Xeon 5160 processor (3.00 GHz, 4MB L2 shared, 1333 MHz system bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4096KB(I+D) on chip, per chip, shared
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6x2GB DDR2-RAM PC2-5300F (CAS 5-5-5)ECC
 Disk Subsystem: 1 x Serial ATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler 9.1
 Build 20060519Z (for 32-bit applications),
 Microsoft Visual Studio 2005 (for libraries),
 MicroQuill SmartHeap for SMP Library 8.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
 186.crafy: -DNT_i386
 252.eon: -DHAS_ERRLIST
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning flags

for C programs: -fast +FDO shlsMPMt.lib
 for C++ programs: -fast -Qcxx_features +FDO

EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from
<http://www.microquill.com/>