



CINT2000 Result

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Fujitsu Siemens Computers

PRIMERGY TX150 S5, Intel Xeon processor 3050, 2.13 GHz

SPECint_rate2000 = 43.7

SPECint_rate_base2000 = 43.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Aug-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	2	107	30.2	2	107	30.2
175.vpr	2	112	28.9	2	112	28.9
176.gcc	2	48.7	52.4	2	48.7	52.4
181.mcf	2	138	30.3	2	138	30.3
186.crafty	2	52.3	44.3	2	52.3	44.3
197.parser	2	121	34.6	2	121	34.6
252.eon	2	47.3	63.8	2	47.3	63.8
253.perlbnk	2	73.0	57.2	2	73.0	57.2
254.gap	2	52.6	48.5	2	52.6	48.5
255.vortex	2	56.5	78.0	2	56.5	78.0
256.bzip2	2	114	30.5	2	114	30.5
300.twolf	2	128	54.4	2	128	54.4

Hardware

CPU: Intel Xeon processor 3050 (2.13 GHz, 2MB L2, 1066 MHz system bus)
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4x2GB DDRII-RAM PC2-4200E (CAS 4-4-4)
Disk Subsystem: Western Digital (SATA, 7.2krpm, 160GB)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries),
SmartHeap Library Version 8.0
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlw32m.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32m.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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Notes/Tuning Information (Continued)

BIOS configuration:

Adjacent Sector Prefetch = disable

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