



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL380 G5 (2.66GHz, Intel Xeon processor 5150)

SPECint_rate2000 = 111
SPECint_rate_base2000 = 110

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	87.6	74.2	4	87.5	74.3
175.vpr	4	84.2	77.2	4	80.3	80.9
176.gcc	4	44.7	114	4	44.7	114
181.mcf	4	85.7	97.5	4	85.7	97.5
186.crafty	4	41.0	113	4	41.0	113
197.parser	4	91.1	91.7	4	90.6	92.2
252.eon	4	37.4	161	4	37.4	161
253.perlbnk	4	58.8	142	4	58.7	142
254.gap	4	48.4	105	4	48.3	106
255.vortex	4	49.0	180	4	49.3	179
256.bzip2	4	88.1	79.0	4	88.1	79.0
300.twolf	4	98.3	142	4	98.3	142

Hardware

CPU: Intel Xeon processor 5150 (2.66GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2048 MB PC2-5300F)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

256.bzip2: basepeak=1
300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled