



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL380 G5 (2.0GHz, Intel Xeon processor 5130)

SPECint_rate2000 = **88.4**
SPECint_rate_base2000 = **88.0**

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	4	115	56.3	4	115	56.3
175.vpr	4	103	62.8	4	98.9	65.7
176.gcc	4	50.9	100	4	50.9	100
181.mcf	4	94.1	88.7	4	94.3	88.5
186.crafty	4	54.7	84.8	4	54.7	84.8
197.parser	4	117	71.6	4	116	71.9
252.eon	4	49.8	121	4	49.8	121
253.perlbnk	4	77.0	108	4	76.7	109
254.gap	4	58.1	87.8	4	57.9	88.2
255.vortex	4	60.6	145	4	60.5	146
256.bzip2	4	110	63.4	4	110	63.4
300.twolf	4	131	106	4	131	106

Hardware

CPU: Intel Xeon processor 5130 (2.0GHz, 4MB L2 shared, 1333MHz bus)
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Parallel: No
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 16 GB (8x2048 MB PC2-5300F)
 Disk Subsystem: 1x36 GB 10 K SAS
 Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
 Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERL DLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.zip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

256.bzip2: basepeak=1
300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled