



CINT2000 Result

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Hewlett-Packard Company
ProLiant BL480c (1.60 GHz, Intel Xeon processor 5110)

SPECint2000 = **1709**
SPECint_base2000 = **1703**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Aug-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	141	996	141	995	[Bar chart showing ratio]			
175.vpr	1400	112	1246	108	1292	[Bar chart showing ratio]			
176.gcc	1100	56.5	1949	56.5	1949	[Bar chart showing ratio]			
181.mcf	1800	65.4	2752	65.4	2752	[Bar chart showing ratio]			
186.crafty	1000	68.1	1469	68.2	1466	[Bar chart showing ratio]			
197.parser	1800	134	1341	134	1345	[Bar chart showing ratio]			
252.eon	1300	62.1	2093	62.3	2086	[Bar chart showing ratio]			
253.perlbnk	1800	92.5	1946	92.1	1955	[Bar chart showing ratio]			
254.gap	1100	65.6	1678	65.5	1679	[Bar chart showing ratio]			
255.vortex	1900	65.9	2885	65.7	2890	[Bar chart showing ratio]			
256.bzip2	1500	119	1259	119	1259	[Bar chart showing ratio]			
300.twolf	3000	164	1830	164	1829	[Bar chart showing ratio]			

Hardware

CPU: Intel Xeon processor 5110 (1.60 GHz, 4 MB L2 shared, 1066 MHz bus)
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2048 MB PC2-5300F)
Disk Subsystem: 2x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode