



# CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

**Hewlett-Packard Company**  
ProLiant DL360 G5 (1.60 GHz, Intel Xeon processor 5110)

SPECint2000 = 1714  
SPECint\_base2000 = 1708

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	141	996	141	994	
175.vpr	1400	112	1251	107	1304	
176.gcc	1100	56.4	1952	56.4	1952	
181.mcf	1800	64.6	2787	64.6	2787	
186.crafty	1000	68.0	1470	68.1	1469	
197.parser	1800	134	1342	134	1347	
252.eon	1300	62.1	2093	62.3	2085	
253.perlbmk	1800	92.5	1945	92.2	1952	
254.gap	1100	65.9	1670	65.7	1675	
255.vortex	1900	65.1	2917	65.2	2916	
256.bzip2	1500	119	1263	119	1263	
300.twolf	3000	164	1833	164	1833	

## Hardware

CPU: Intel Xeon processor 5110 (1.60 GHz, 4 MB L2 shared, 1066 MHz bus)  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 4 MB I+D on chip per chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 GB (8x1024 MB PC2-5300F)  
Disk Subsystem: 1x36 GB 10 K SAS  
Other Hardware:

## Software

Operating System: Windows Server 2003 Standard Edition SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERL DLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



# CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

**Hewlett-Packard Company**  
ProLiant DL360 G5 (1.60 GHz, Intel Xeon processor 5110)

SPECint2000 = 1714  
SPECint\_base2000 = 1708

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006

## Notes/Tuning Information (Continued)

256.bzip2: basepeak=1  
300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode