



# CFP2000 Result

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## Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7030, 2.80 GHz

SPECfp\_rate2000 = 51.1

SPECfp\_rate\_base2000 = 51.1

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jun-2006 | Hardware Avail: May-2006 | Software Avail: Jan-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	197	75.5	8	197	75.5
171.swim	8	775	37.1	8	775	37.1
172.mgrid	8	523	31.9	8	523	31.9
173.applu	8	593	32.9	8	593	32.9
177.mesa	8	106	123	8	106	123
178.galgel	8	360	74.7	8	360	74.7
179.art	8	421	57.3	8	421	57.3
183.quake	8	404	29.9	8	404	29.9
187.facerec	8	331	53.2	8	331	53.2
188.amp	8	355	57.5	8	355	57.5
189.lucas	8	448	41.4	8	448	41.4
191.fma3d	8	446	43.7	8	446	43.7
200.sixtrack	8	215	47.4	8	215	47.4
301.apsi	8	358	67.4	8	358	67.4

### Hardware

CPU: Intel Xeon processor 7030 (2.80 GHz, 2x1MB L2, 800 MHz system bus)  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)  
 CPU(s) orderable: 1,2,4  
 Parallel: No  
 Primary Cache: 12k micro-ops I + 16KBD on chip, per core  
 Secondary Cache: 1024KB(I+D) on chip, per core  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)  
 Disk Subsystem: Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)  
 Other Hardware: none

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 9 with SP3  
 Kernel 2.6.5-7.244-smp on an x86\_64  
 Compiler: Intel C++ and Fortran Compiler 9.0 for EM64T  
 Build 20060120 (for 64-bit applications)  
 File System: ext3  
 System State: Multi-user run level 3

## Notes/Tuning Information

### GENERAL

+FDO implies feedback-directed optimization  
 PASS1: -prof\_gen PASS2: -prof\_use

### Optimization flags

ONESTEP=yes set for all benchmarks

### Portability flags

-DSPEC\_CPU2000\_LP64 applied to all benchmarks  
 178.galgel: -FI for fixed-format Fortran

### Base tuning flags

for Fortran and C programs: -fast +FDO

### Peak tuning flags

same as baseline (basepeak=true set globally)



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## Notes/Tuning Information (Continued)

The system bus runs at 800 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

This result was measured on the PRIMERGY TX600 S3. The PRIMERGY TX600 S3 and the PRIMERGY RX600 S3 are electronically equivalent.

BIOS Configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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<http://www.fujitsu-siemens.com/countries>