



CFP2000 Result

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Hewlett-Packard Company
ProLiant DL380 G5 (3.73GHz, Intel Xeon Processor 5080)

SPECfp_rate2000 = 61.8
SPECfp_rate_base2000 = 61.6

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	76.6	96.9	4	76.4	97.1
171.swim	4	293	49.1	4	293	49.1
172.mgrid	4	184	45.5	4	184	45.5
173.applu	4	207	47.0	4	207	47.0
177.mesa	4	87.5	74.2	4	87.1	74.6
178.galgel	4	91.1	148	4	91.1	148
179.art	4	95.9	126	4	95.9	126
183.quake	4	100	60.2	4	100	60.2
187.facerec	4	134	65.9	4	134	66.0
188.amp	4	206	49.5	4	200	51.0
189.lucas	4	200	46.3	4	198	47.0
191.fma3d	4	198	49.2	4	198	49.2
200.sixtrack	4	151	33.8	4	151	33.8
301.apsi	4	226	53.5	4	226	53.5

Hardware

CPU: Intel Xeon Processor 5080 (3.73GHz, 2x2MB L2, 1066MHz bus)
CPU MHz: 3730
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12K micro-ops I + 16KBD (on chip) per core
Secondary Cache: 2048KB(I+D) (on chip) per core
L3 Cache: N/A
Other Cache: N/A
Memory: 4x2048MB PC2-5300F
Disk Subsystem: 1x36GB 10K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran programs: -fast -Qansi_alias +FDO
Base tuning for C programs: -fast +FDO shlw32M.lib
Portability flags:
 178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast +FDO ONESTEP=yes
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=1
177.mesa: -fast +FDO ONESTEP=yes
178.galgel: basepeak=1
179.art: basepeak=1
183.quake: -fast -Oa -Qrcd +FDO ONESTEP=yes shlw32M.lib
187.facerec: -fast -Qunroll1 -Qscalar_rep- +FDO ONESTEP=yes
188.amp: -fast -Qansi_alias +FDO
189.lucas: -fast
```



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Notes/Tuning Information (Continued)

191.fma3d: basepeak=1
200.sixtrack: -fast +FDO
301.apsi: basepeak=1
BIOS Configuration Notes
Hyper-Threading Technology disabled
Power Regulator set to Static High Performance Mode