



CINT2000 Result

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HITACHI

HITACHI BladeSymphony (1.66GHz/9MB Itanium 2, 4Blades 8waySMP)

SPECint_rate2000 = --

SPECint_rate_base2000 = 138

SPEC license #: 872 | Tested by: HITACHI | Test date: Dec-2005 | Hardware Avail: Nov-2005 | Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	8	133	97.9			
175.vpr	8	112	116			
176.gcc	8	63.7	160			
181.mcf	8	67.1	249			
186.crafty	8	74.0	125			
197.parser	8	168	99.7			
252.eon	8	57.4	210			
253.perlbnk	8	125	134			
254.gap	8	120	85.2			
255.vortex	8	83.9	210			
256.bzip2	8	115	121			
300.twolf	8	206	135			

Hardware

CPU: Intel Itanium 2 (667MHz FSB)
 CPU MHz: 1666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 8 chips, 1 core/chip
 CPU(s) orderable: 1,2,4,8
 Parallel: No
 Primary Cache: 16KBI + 16KBD on chip, per core
 Secondary Cache: 256KB(I+D) on chip, per core
 L3 Cache: 9.0MB(I+D) on chip, per core
 Other Cache: N/A
 Memory: 32GB (1GB DIMM x 32)
 Disk Subsystem: Seagate Ultra320 ST373454LC (73GBx1), 15000rpm
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux AS 4 update 1
 Compiler: Intel(R) C++ Compiler for Linux 9.0 (Build 20051020)
 File System: ext3
 System State: Multi-user run level 3

Notes/Tuning Information

Base tuning flags:

C : -fast -auto_ilp32 +FDO
 C++ : -fast -auto_ilp32 -IPF_fp_relaxed +FDO

+FDO: PASS1=-prof_gen PASS2=-prof_use

Portability flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_alloca
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST fmax_errno src.alt
 253.perlbnk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64 closed_files src.alt
 300.twolf: stdfree src.alt

Processes were bound to CPUs by using HITACHI bindlp command.
 ccNUMA configuration was selected by firmware.