



CINT2000 Result

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Acer Incorporated

Acer Altos R710 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint_rate2000 = 42.0

SPECint_rate_base2000 = 41.8

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Oct-2005 | Hardware Avail: Oct-2005 | Software Avail: May-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	170	38.3	4	170	38.3
175.vpr	4	242	26.9	4	238	27.3
176.gcc	4	88.1	57.9	4	88.1	57.9
181.mcf	4	370	22.6	4	370	22.6
186.crafty	4	140	33.1	4	140	33.1
197.parser	4	199	42.1	4	198	42.2
252.eon	4	111	54.6	4	111	54.4
253.perlbnk	4	172	48.5	4	173	48.4
254.gap	4	112	45.7	4	112	45.7
255.vortex	4	127	69.4	4	127	69.4
256.bzip2	4	203	34.3	4	196	35.5
300.twolf	4	262	53.2	4	263	52.9

Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
 CPU MHz: 3800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology Enable)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
 Secondary Cache: 2048KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6 x 1024MB ECC DDR2-400 SDRAM
 Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
 Compiler: Intel C++ Compiler 9.0 Build 20050430Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
 shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>