



CINT2000 Result

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Acer Incorporated
Altos G520 (3.0 GHz Intel Xeon)

SPECint2000 = 1430
SPECint_base2000 = 1427

SPEC license #: 97 Tested by: Acer Incorporated Test date: Jun-2005 Hardware Avail: May-2005 Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	140	1000	140	1000	[Bar chart showing ratio 1000]			
175.vpr	1400	142	986	140	1000	[Bar chart showing ratio 1000]			
176.gcc	1100	65.2	1688	65.2	1688	[Bar chart showing ratio 1688]			
181.mcf	1800	113	1592	113	1592	[Bar chart showing ratio 1592]			
186.crafty	1000	89.5	1117	89.5	1117	[Bar chart showing ratio 1117]			
197.parser	1800	147	1225	147	1225	[Bar chart showing ratio 1225]			
252.eon	1300	69.2	1878	69.0	1883	[Bar chart showing ratio 1883]			
253.perlbmk	1800	103	1740	103	1741	[Bar chart showing ratio 1741]			
254.gap	1100	68.2	1614	68.2	1614	[Bar chart showing ratio 1614]			
255.vortex	1900	80.7	2355	80.7	2355	[Bar chart showing ratio 2355]			
256.bzip2	1500	142	1053	142	1060	[Bar chart showing ratio 1060]			
300.twolf	3000	200	1499	200	1499	[Bar chart showing ratio 1499]			

Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1024MB ECC DDR-333 SDRAM
Disk Subsystem: 1 x 120GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 8.1 Build 20041019Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbmk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
For PASS1_CFLAGS of 300.twolf -fast also implicitly calls out -O3
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>