



CINT2000 Result

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Acer Incorporated
Altos R710 (3.0 GHz Intel Xeon)

SPECint_rate2000 = 32.8
SPECint_rate_base2000 = 32.7

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Oct-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	141	23.0	2	141	23.0
175.vpr	2	141	23.0	2	139	23.3
176.gcc	2	65.3	39.1	2	65.3	39.1
181.mcf	2	122	34.3	2	122	34.3
186.crafty	2	89.5	25.9	2	89.5	25.9
197.parser	2	146	28.6	2	146	28.6
252.eon	2	69.3	43.6	2	69.0	43.7
253.perlbnk	2	103	40.4	2	103	40.5
254.gap	2	74.5	34.2	2	74.5	34.2
255.vortex	2	79.3	55.6	2	79.3	55.6
256.bzip2	2	141	24.6	2	140	24.8
300.twolf	2	200	34.8	2	200	34.8

Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 73GB 10000RPM U320 SCSI HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 8.1 Build 20041019Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>