



CFP2000 Result

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Acer Incorporated
Altos R510 (3.0 GHz Intel Xeon)

SPECfp_rate2000 = 28.1
SPECfp_rate_base2000 = 27.8

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2005 Hardware Avail: May-2005 Software Avail: Oct-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	85.0	43.7	2	85.0	43.7
171.swim	2	300	24.0	2	300	24.0
172.mgrid	2	190	22.0	2	190	22.0
173.applu	2	234	20.8	2	234	20.8
177.mesa	2	107	30.3	2	107	30.3
178.galgel	2	118	57.2	2	117	57.3
179.art	2	94.7	63.7	2	94.7	63.7
183.quake	2	93.4	32.3	2	93.4	32.3
187.facerec	2	136	32.5	2	136	32.5
188.amp	2	247	20.7	2	209	24.4
189.lucas	2	200	23.3	2	199	23.3
191.fma3d	2	222	21.9	2	222	21.9
200.sixtrack	2	203	12.5	2	202	12.7
301.apsi	2	272	22.2	2	272	22.2

Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology disabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
 Secondary Cache: 2048KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6 x 512MB ECC DDR-333 SDRAM
 Disk Subsystem: 1 x 80GB 7200RPM SATA HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
 Compiler: Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z
 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
 SmartHeap Library Version 7.4 from http://www.microquill.com/
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran Programs : -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.quake:Base for C Program: -fast +FDO shlw32M.lib
188.amp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```



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Notes/Tuning Information (Continued)

187.facerec: basepeak=yes
188.ammf: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
191.fma3d: basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi: -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>