



CINT2000 Result

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Acer Incorporated
Altos G520 (3.6 GHz Intel Xeon)

SPECint2000 = 1515
SPECint_base2000 = 1512

SPEC license #: 97 Tested by: Acer Incorporated Test date: Apr-2005 Hardware Avail: Apr-2005 Software Avail: Apr-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	117	1192	118	1185	
175.vpr	1400	154	912	151	929	
176.gcc	1100	60.5	1818	60.5	1818	
181.mcf	1800	161	1118	161	1118	
186.crafty	1000	75.0	1333	75.3	1328	
197.parser	1800	136	1320	136	1322	
252.eon	1300	57.4	2264	57.2	2272	
253.perlbnk	1800	88.8	2027	88.3	2039	
254.gap	1100	58.6	1878	58.6	1878	
255.vortex	1900	77.3	2457	77.3	2457	
256.bzip2	1500	148	1016	147	1019	
300.twolf	3000	176	1701	176	1706	

Hardware

CPU: Intel Xeon (3.6 GHz, 1MB L2, 800MHz system bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1024MB ECC DDR-333 SDRAM
Disk Subsystem: 1 x 80GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 8.1 Build 20041019Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast -Qansi_alias -Oa +FDO
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: -fast -Qansi_alias -Oa +FDO
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll11 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>