



CINT2000 Result

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Hewlett-Packard Company
HP Integrity rx1620-2 (1.3GHz/3MB Itanium 2)

SPECint2000 = 1178
SPECint_base2000 = 1178

SPEC license #: 3 Tested by: HP Richardson Test date: Dec-2004 Hardware Avail: Jan-2005 Software Avail: Jan-2005

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio | |
|-------------|----------------|--------------|------------|---------|-------|--|
| 164.gzip | 1400 | 160 | 877 | 160 | 877 | |
| 175.vpr | 1400 | 145 | 964 | 145 | 964 | |
| 176.gcc | 1100 | 76.4 | 1440 | 76.4 | 1440 | |
| 181.mcf | 1800 | 151 | 1194 | 151 | 1194 | |
| 186.crafty | 1000 | 93.9 | 1066 | 93.9 | 1066 | |
| 197.parser | 1800 | 205 | 876 | 205 | 876 | |
| 252.eon | 1300 | 80.5 | 1615 | 80.5 | 1615 | |
| 253.perlbmk | 1800 | 153 | 1176 | 153 | 1176 | |
| 254.gap | 1100 | 107 | 1029 | 107 | 1029 | |
| 255.vortex | 1900 | 105 | 1809 | 105 | 1809 | |
| 256.bzip2 | 1500 | 133 | 1131 | 133 | 1131 | |
| 300.twolf | 3000 | 227 | 1320 | 227 | 1320 | |

Hardware

CPU: Intel Itanium 2 (1.3GHz/3MB, 400MHz FSB)
CPU MHz: 1300
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1,2
Parallel: no
Primary Cache: 16KBI + 16KBD (on chip) per CPU
Secondary Cache: 256KB (on chip) per CPU
L3 Cache: 3.0MB (on chip) per CPU
Other Cache: N/A
Memory: 16GB (8x2GB DIMMs)
Disk Subsystem: 1x36GB 15K rpm SCSI
Other Hardware: N/A

Software

Operating System: HPUX11i-TCOE B.11.23.0409
Compiler: HP C/ANSI C Compiler C.06.00
HP aC++ Compiler C.06.00
File System: vxfs
System State: Multi-user

Notes/Tuning Information

Portability Flags

```
176.gcc      : -DHOST_WORDS_BIG_ENDIAN
186.crafty   : -DHP
252.eon      : -DFMAX_IS_DOUBLE
253.perlbmk  : -DSPEC_CPU2000_HP
254.gap      : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO
```

Base Flags

```
all         : +Oprofile=collect:all/+Oprofile=use
C           : +Ofaster -exec +Otype_safety=ansi
C++        : +Ofaster +Otype_safety=ansi
```

Peak Flags

same as base (basepeak=true set globally)

Kernel Tunables:

```
dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
```



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Notes/Tuning Information (Continued)

```
maxssiz=401604608
maxdsiz_64bit=4396972761584
maxssiz_64bit=1073741824
vps_pagesize=4096
vps_ceiling=16384
```

Notes:

The system under test had the HP-UX 11i v2 (version 11.23.0409, September, 2004) Technical Computing Operating Environment and AR1204 compilers installed, along with the following patches:

```
PHSS_31849 linker + fdp cumulative patch
PHSS_31850 assembler patch
PHSS_31851 Integrity Unwind Library
PHSS_31852 aC++ Runtime (IA: A.05.60, PA A.03.60)
PHSS_31853 Math Library Cumulative Patch
PHSS_31854 milli cumulative patch
```

EFI command "cpuconfig" used prior to booting to deconfigure other processors if necessary