



CINT2000 Result

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Hewlett-Packard Company
ProLiant ML350 G4 (3.4GHz, Intel Xeon)

SPECint_rate2000 = 16.8
SPECint_rate_base2000 = 16.5

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Nov-2004 Hardware Avail: Aug-2004 Software Avail: Sep-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	124	13.1	1	124	13.1
175.vpr	1	154	10.5	1	149	10.9
176.gcc	1	63.0	20.2	1	63.0	20.2
181.mcf	1	159	13.2	1	159	13.2
186.crafty	1	79.5	14.6	1	79.5	14.6
197.parser	1	142	14.7	1	142	14.7
252.eon	1	77.5	19.4	1	63.9	23.6
253.perlbnk	1	102	20.4	1	101	20.7
254.gap	1	64.2	19.9	1	63.5	20.1
255.vortex	1	76.4	28.8	1	76.4	28.8
256.bzip2	1	148	11.8	1	147	11.8
300.twolf	1	183	19.0	1	183	19.0

Hardware

CPU: Intel Xeon (3.4GHz, 1MB L2, 800MHz bus)
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology disabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12K micro-ops I + 16KBD on chip
 Secondary Cache: 1024KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 4x2048MB PC2700 CL2.5
 Disk Subsystem: 1x72.8GB 10K U320
 Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
 Compiler: Intel C++ Compiler 8.1 Build 20040802Z
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
+FDO*: PASS1=-Qprof_gen PASS2=-Qprof_use (-QxP not used in PASS1 to enable cross-compilation)
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -GX -GR
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
 164.gzip: basepeak=1
 175.vpr: -fast +FDO ONESTEP=yes
 176.gcc: basepeak=1
 181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
 186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
 197.parser: -fast +FDO ONESTEP=yes
 252.eon: -QxP -O2 -Qipo +FDO* ONESTEP=yes
 253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
 254.gap: -fast +FDO ONESTEP=yes
 255.vortex: basepeak=1
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll11 +FDO ONESTEP=yes
300.twolf: basepeak=1
BIOS Configuration Notes
HT Technology disabled