



# CINT2000 Result

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**SGI**

SGI Altix 3700 Bx2 (1600MHz 9M L3, Itanium 2)

SPECint2000 = --

SPECint\_base2000 = 1441

SPEC license #: 4 Tested by: SGI Test date: Oct-2004 Hardware Avail: Nov-2004 Software Avail: Nov-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000	2000	3000	4000
164.gzip	1400	131	1069						
175.vpr	1400	117	1200						
176.gcc	1100	68.8	1600						
181.mcf	1800	67.5	2665						
186.crafty	1000	77.8	1285						
197.parser	1800	172	1046						
252.eon	1300	65.6	1982						
253.perlbmk	1800	133	1349						
254.gap	1100	114	968						
255.vortex	1900	85.2	2231						
256.bzip2	1500	120	1251						
300.twolf	3000	198	1514						

## Hardware

CPU: Intel Itanium 2  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
CPU(s) orderable: 16-256  
Parallel: No  
Primary Cache: 16KBI + 16KBD on chip, per core  
Secondary Cache: 256KB (I+D) on chip, per core  
L3 Cache: 9.0MB (I+D) on chip, per core  
Other Cache: N/A  
Memory: 8 GB (1GB PC2700 DIMMS)  
Disk Subsystem: 16 x 73 GB FibreChannel (Seagate Cheetah 15k rpm)  
Other Hardware: None

## Software

Operating System: SGI ProPack(TM) v3.0 Service Pack 1  
Compiler: Intel(R) C++ Compiler for Linux 8.1 (Build 20041021)  
File System: MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
System State: xfs  
Single-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

### Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
C++ programs: -fast -ansi\_alias +FDO  
Extra Libraries: libsmartheap64.a

### Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca  
186.crafty: -DLINUX\_i386  
252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
253.perlbmk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
-DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
-DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
255.vortex: -DSPEC\_CPU2000\_LP64 srcalt=closed\_files

Extra CPUs were disabled at firmware.  
Processes were bound to CPUs using dplace.