



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL380 G4 (3.4GHz, Intel Xeon)

SPECint_rate2000 = 31.5
SPECint_rate_base2000 = 30.6

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Aug-2004 Software Avail: Aug-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	128	25.5	2	128	25.5
175.vpr	2	172	18.9	2	164	19.8
176.gcc	2	66.7	38.2	2	66.7	38.2
181.mcf	2	232	18.0	2	228	18.4
186.crafty	2	79.8	29.1	2	79.9	29.1
197.parser	2	147	28.3	2	147	28.4
252.eon	2	77.9	38.7	2	65.3	46.2
253.perlbnk	2	105	39.7	2	102	40.8
254.gap	2	72.3	35.3	2	71.0	35.9
255.vortex	2	80.0	55.1	2	79.7	55.3
256.bzip2	2	161	21.6	2	158	22.0
300.twolf	2	179	38.8	2	179	39.0

Hardware

CPU: Intel Xeon (3.4GHz, 1MB L2, 800MHz bus)
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (HT Technology disabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 16K micro-ops I + 16KBD on chip
 Secondary Cache: 1024KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 4x512MB DDR2-400
 Disk Subsystem: 1x18.2GB 15K Ultra320 SCSI
 Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
 Compiler: Intel C++ Compiler 8.0 Build 20031211Z
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
 +FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for C programs: -QxP -fast +FDO* shlw32M.lib

Base tuning for C++ programs: -QxP -fast -GX -GR

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -QxP -fast -Oa +FDO ONESTEP=yes

175.vpr: -QxP -fast +FDO ONESTEP=yes

176.gcc: basepeak = 1

181.mcf: -QxP -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -QxP -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -QxP -fast +FDO ONESTEP=yes

252.eon: -QxP -O2 -Qipo +FDO* ONESTEP=yes

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -QxP -fast +FDO ONESTEP=yes

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http://www.spec.org



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Notes/Tuning Information (Continued)

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255.vortex:  -QxP -fast          +FDO  ONESTEP=yes  shlw32M.lib
256.bzip2:   -fast -Oa -Qunroll1 +FDO  ONESTEP=yes
300.twolf:   -QxP -fast -O3      +FDO*          shlw32M.lib
```

BIOS Configuration Notes
HT Technology disabled
Adjacent Sector Prefetch disabled