



# CINT2000 Result

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**Supermicro**  
SUPER P4SCT+II(3.4 GHz, Pentium 4 processor)

SPECint2000 = 1493  
SPECint\_base2000 = 1431

SPEC license #01176 Tested by: Supermicro Inc., San Jose, CA Test date: Jun-2004 Hardware Avail: Jun-2004 Software Avail: Dec-2003

| Benchmark   | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |  |
|-------------|----------------|--------------|------------|---------|-------|--|
| 164.gzip    | 1400           | 124          | 1125       | 124     | 1125  |  |
| 175.vpr     | 1400           | 143          | 979        | 143     | 979   |  |
| 176.gcc     | 1100           | 60.0         | 1834       | 60.0    | 1834  |  |
| 181.mcf     | 1800           | 144          | 1251       | 143     | 1262  |  |
| 186.crafty  | 1000           | 81.7         | 1224       | 79.9    | 1251  |  |
| 197.parser  | 1800           | 136          | 1322       | 136     | 1322  |  |
| 252.eon     | 1300           | 78.1         | 1664       | 66.7    | 1949  |  |
| 253.perlbmk | 1800           | 111          | 1619       | 100     | 1795  |  |
| 254.gap     | 1100           | 61.4         | 1792       | 61.4    | 1792  |  |
| 255.vortex  | 1900           | 75.1         | 2531       | 75.1    | 2531  |  |
| 256.bzip2   | 1500           | 141          | 1062       | 137     | 1097  |  |
| 300.twolf   | 3000           | 216          | 1386       | 180     | 1670  |  |

### Hardware

CPU: Pentium 4 processor (3.4 GHz, 800 MHz bus)  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology enabled)  
 CPU(s) orderable: 1  
 Parallel: No  
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip  
 Secondary Cache: 512KB(I+D) on chip  
 L3 Cache: 1024KB(I+D) on chip  
 Other Cache: N/A  
 Memory: 4 x 256MB Unbuffered CL2.5 PC 3200 DDR SDRAM  
 Disk Subsystem: 1 x Seagate 40GB IDE  
 Other Hardware:

### Software

Operating System: Windows XP Professional W/ SP1a  
 Compiler: Intel C/C++ Compiler 8.0 (20031229Z)  
 Microsoft Visual Studio .NET (7.0.9466)  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
 186.crafy: -DNT\_i386  
 253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
 254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### FEEDBACK DIRECTED OPTIMIZATION

ONESTEP=yes  
 FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### BASE TUNING

C: -QxN -Qipo -O3 +FDO  
 C++: -QxN -Qipo -GX -GR

### PEAK TUNING

164.gzip: -QxN -Qipo -O3 -Oa +FDO  
 175.vpr: basepeak=yes  
 176.gcc: basepeak=yes  
 181.mcf: -QxN -Qipo -O3 +FDO shlw32M.lib  
 186.crafty: -QxN -Qipo -O3 -Oa +FDO shlw32M.lib  
 197.parser: basepeak=yes  
 252.eon: -QxN -Qipo -O3 +FDO  
 253.perlbmk: -QxN -Qipo -O3 +FDO shlw32M.lib



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## Notes/Tuning Information (Continued)

```
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:    -Qipo      -Oa -Qunroll1 +FDO shlW32M.lib
300.twolf:    -QxN -Qipo -O3      +FDO shlW32M.lib
EXTRA LIBRARIES
  shlW32M.lib: MicroQuill SmartHeap Library 7.1
                www.microquill.com
Used the "-u 2" option on the runspec command
Up to 4GB of Unbuffered DDR-400/333/266 memory
Hyper threading Enabled
ATX form factor optimized for 1U/2U/4U/Tower chassis
See Link for more chassis compatibility lists at
http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm.
```