



CINT2000 Result

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SGI
SGI Altix 350 (1000MHz, Itanium 2)

SPECint2000 = 743
SPECint_base2000 = 743

SPEC license #: 4 Tested by: SGI Test date: Feb-2004 Hardware Avail: Jan-2004 Software Avail: Jan-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	222	630	222	630	
175.vpr	1400	244	574	244	574	
176.gcc	1100	132	830	132	830	
181.mcf	1800	263	684	263	684	
186.crafty	1000	127	790	127	790	
197.parser	1800	323	558	323	558	
252.eon	1300	128	1014	128	1014	
253.perlbnk	1800	232	776	232	776	
254.gap	1100	179	614	179	614	
255.vortex	1900	156	1217	156	1217	
256.bzip2	1500	228	658	228	658	
300.twolf	3000	371	809	371	809	

Hardware

CPU: Intel Itanium 2
 CPU MHz: 1000
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 2-16
 Parallel: No
 Primary Cache: 16KBI + 16KBD (on chip) per CPU
 Secondary Cache: 256KB (on chip) per CPU
 L3 Cache: 1.5MB (on chip) per CPU
 Other Cache: N/A
 Memory: 4 GB (8*512MB PC2700 DIMMS)
 Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
 Other Hardware: None

Software

Operating System: SGI ProPack(TM) v2.4
 Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20040101)
 MicroQuill SmartHeap Library 7.01 (www.microquill.com)
 File System: xfs
 System State: Single-user

Notes/Tuning Information

+FDO: PASS1=-prof_gen PASS2=-prof_use

Baseline optimization flags:

C programs: -fast -auto_ilp32 +FDO
 C++ programs: -fast -ansi_alias +FDO
 Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
 253.perlbnk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).