



CFP2000 Result

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Supermicro
SUPER X5DPA-GG (3.06GHz, 533MHz bus)

SPECfp_rate2000 = 12.4
SPECfp_rate_base2000 = 12.1

SPEC license #01176 | Test supply: Micro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	176	21.1	2	186	20.0
171.swim	2	608	11.8	2	609	11.8
172.mgrid	2	448	9.32	2	447	9.35
173.applu	2	444	11.0	2	443	11.0
177.mesa	2	174	18.6	2	168	19.4
178.galgel	2	370	18.2	2	378	17.8
179.art	2	1028	5.87	2	983	6.13
183.quake	2	227	13.3	2	179	16.9
187.facerec	2	236	18.7	2	229	19.3
188.amp	2	628	8.12	2	630	8.10
189.lucas	2	404	11.5	2	400	11.6
191.fma3d	2	367	13.3	2	367	13.3
200.sixtrack	2	246	10.4	2	234	10.9
301.apsi	2	665	9.06	2	648	9.30

Hardware

CPU: Intel Xeon (3.06GHz, 533MHz bus)
CPU MHz: 3066
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
Secondary Cache: 512KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem: 1 x IBM Deskstar 123.5GB IDE
Other Hardware:

Software

Operating System: Windows XP Professional W/ SP1a
Compiler: Intel C++ and Fortran Compiler 7.1 Build 20030224Z
Microsoft Visual Studio Enterprise Edition 6.0(for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning: -QxW -Qipo -O3 +FDO
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -Qipo -QxW +FDO
171.swim: -Qipo -QxW -O3 +FDO
172.mgrid: -Qipo -QaxW -O3 +FDO
173.applu: -Qipo -QxW -O3 -Qscalar_rep- +FDO
177.mesa: -Qipo -QxW -O3 +FDO
178.galgel: -Qipo -QxW -O3 +FDO
179.art: -Qipo -Oa -Qunroll14 -Zp4
183.quake: -Qipo -QxW -Oa +FDO shlw32M.lib
187.facerec: -Qipo -QxW -O3 -Qunroll11 -Qscalar_rep- +FDO
188.amp: -QxW -Oa
189.lucas: -Qipo -QaxW
191.fma3d: basepeak=1
```



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Notes/Tuning Information (Continued)

200.sixtrack: -Qipo -QxW +FDO

301.apsi: -Qipo -QxW -O3 +FDO

Hyper-Threading Technology is turned ON

ONESTEP is used for all base and peak runs

The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350, with 350W power supply. See Link for more chassis compatibility lists at <http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm>.

The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.

shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com