



# CINT2000 Result

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Hewlett-Packard Company  
HP Integrity rx8620 (1300 MHz Itanium 2)

SPECint\_rate2000 = 92.7  
SPECint\_rate\_base2000 = 91.3

SPEC license #: 3 Tested by: HP Richardson Test date: Nov-2003 Hardware Avail: Nov-2003 Software Avail: Oct-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	170	76.2	8	170	76.2
175.vpr	8	167	77.8	8	163	79.9
176.gcc	8	92.9	110	8	92.9	110
181.mcf	8	240	69.7	8	240	69.7
186.crafty	8	95.4	97.3	8	93.6	99.1
197.parser	8	232	71.9	8	232	71.9
252.eon	8	98.4	123	8	94.7	127
253.perlbnk	8	179	93.1	8	172	97.1
254.gap	8	138	74.2	8	138	74.2
255.vortex	8	127	139	8	124	142
256.bzip2	8	160	87.3	8	160	87.3
300.twolf	8	270	103	8	263	106

### Hardware

CPU: Intel Itanium 2  
CPU MHz: 1300  
FPU: Integrated  
CPU(s) enabled: 8 cores, 8 chips, 1 core/chip  
CPU(s) orderable: 2,4,6,8,10,12,14,16  
Parallel: no  
Primary Cache: 16KBI + 16KBD (on chip) per CPU  
Secondary Cache: 256KB (on chip) per CPU  
L3 Cache: 3.0MB (on chip) per CPU  
Other Cache: N/A  
Memory: 32GB (64 \* 512MB DIMMs)  
Disk Subsystem: 10x36GB 10K rpm SCSI (striped)  
Other Hardware: N/A

### Software

Operating System: HPUX11i-TCOE B.11.23  
Compiler: HP C/ANSI C Compiler A.05.52  
HP aC++ Compiler A.05.52  
File System: vxfs  
System State: Multi-user

## Notes/Tuning Information

### Portability Flags

176.gcc : -DHOST\_WORDS\_BIG\_ENDIAN  
186.crafty : -DHP  
252.eon : -DFMAX\_IS\_DOUBLE  
253.perlbnk : -DSPEC\_CPU2000\_HP  
254.gap : -DSPEC\_CPU2000\_HP -DSYS\_IS\_USG -DSYS\_HAS\_IOCTL\_PROTO  
-DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_CALLOC\_PROTO

### Base Flags

all : +Oprofile=collect:all/+Oprofile=use  
C : +Ofaster -exec +Odatalayout  
C++ : +Ofaster +inline\_level 2 -minshared

### Peak Flags

164.gzip : basepeak=true  
175.vpr : +Oprofile=collect:all/+Oprofile=use +Ofaster -exec +Odatalayout  
+Onoptrs\_to\_globals +Onorecovery  
176.gcc : basepeak=true  
181.mcf : basepeak=true  
186.crafty : +Oprofile=collect:all/+Oprofile=use +Ofaster -exec +Odatalayout



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## Notes/Tuning Information (Continued)

```

+Otype_safety=ansi
197.parser : basepeak=true
252.eon : +Oprofile=collect:all/+Oprofile=use +Ofaster +inline_level 2
        -minshared +Otype_safety=ansi
253.perlbnk : +Oprofile=collect:all/+Oprofile=use +Ofaster -exec +Odatalayout
        +Onoptrs_to_globals
        LIBS = /opt/langtools/lib/hpux32/fastmem.o
254.gap : basepeak=true
255.vortex : +Oprofile=collect:all/+Oprofile=use +Ofaster -exec +Odatalayout
        +Onoptrs_to_globals +Onorecovery
256.bzip2 : basepeak=true
300.twolf : +Oprofile=collect:all/+Oprofile=use +Ofaster -exec +Odatalayout
        +Onoptrs_to_globals
        LIBS = /opt/langtools/lib/hpux32/effmem.o

```

### Kernel Tunables:

```

dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
maxdsiz_64bit=4396972761584
maxssiz_64bit=1073741824
vps_pagesize=4096
vps_ceiling=16384

```

The system under test had the HP-UX 11i v2 (version 11.23) Technical Computing Operating Environment and compilers installed, with the following patches applied:

```

PHSS_29655 aC++ compiler (A.05.52)
PHSS_29656 HP C Compiler (A.05.52)
PHSS_29657 u2comp/be/plugin library Patch
PHSS_29663 Fortran Product Patch, v2.7 to v2.7.2

```

### Notes:

System was configured with 1/2 of memory interleaved and 1/2 of memory local to each cell

System configured as a single partition with 4 cells and 2 processors per cell (unused processors disabled prior to booting with firmware command "cpuconfig")

Processes were assigned to localities using the HP-UX mpsched utility

Filesystem used for spec runs mounted "tmplog"