



CINT2000 Result

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Dell
PowerEdge 1750 (3.06 GHz Xeon)

SPECint_rate2000 = 12.4

SPECint_rate_base2000 = 12.0

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Jun-2003 | Hardware Avail: Jul-2003 | Software Avail: Apr-2003

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 1 | 117 | 13.9 | 1 | 117 | 13.9 |
| 175.vpr | 1 | 240 | 6.77 | 1 | 240 | 6.76 |
| 176.gcc | 1 | 83.5 | 15.3 | 1 | 83.4 | 15.3 |
| 181.mcf | 1 | 283 | 7.39 | 1 | 278 | 7.51 |
| 186.crafy | 1 | 89.9 | 12.9 | 1 | 89.9 | 12.9 |
| 197.parser | 1 | 181 | 11.5 | 1 | 181 | 11.6 |
| 252.eon | 1 | 96.3 | 15.7 | 1 | 83.0 | 18.2 |
| 253.perlbnk | 1 | 138 | 15.1 | 1 | 134 | 15.6 |
| 254.gap | 1 | 73.7 | 17.3 | 1 | 73.7 | 17.3 |
| 255.vortex | 1 | 116 | 19.0 | 1 | 116 | 19.0 |
| 256.bzip2 | 1 | 198 | 8.79 | 1 | 192 | 9.07 |
| 300.twolf | 1 | 440 | 7.91 | 1 | 369 | 9.43 |

Hardware

CPU: Intel Pentium Xeon (533 MHz system bus)
 CPU MHz: 3066
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
 Secondary Cache: 512KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 4 X 512MB DDR266
 Disk Subsystem: 1 x 17GB Ultra3 Seagate SCSI
 Other Hardware:

Software

Operating System: Windows 2000 Server (SP2)
 Compiler: Intel C++ Compiler 7.1 (20030402Z)
 Microsoft Visual Studio .NET (7.0.9466)
 MicroQuill SmartHeap Library 6.01
 File System: NTFS
 System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
 186.crafy: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof_gen PASS2=-Qprof_use

BASE TUNING

C: -QxW -Qipo -O3 +FDO
 C++: -QxW -Qipo -GX -GR

PEAK TUNING

164.gzip: -QxW -Qipo -Oa -O3 +FDO
 175.vpr: -QxW -Qipo -O3 +FDO
 176.gcc: -QxW -Qipo -O3 +FDO
 181.mcf: -QxW -Qipo -O3 +FDO shlw32M.lib
 186.crafy: PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
 197.parser: -QxW -Qipo -O3 +FDO
 252.eon: PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
 253.perlbnk: -QxW -Qipo -O3 +FDO shlw32M.lib
 254.gap: basepeak=yes



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Notes/Tuning Information (Continued)

```
255.vortex    basepeak=yes
256.bzip2:    -Qipo -Oa -Qunroll1 +FDO
300.twolf:    -Qipo -QxW -O3 -Oa      shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib:  MicroQuill SmartHeap Library 6.01
              www.microquill.com
ONESTEP=YES is set for all benchmarks
```