



CFP2000 Result

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Advanced Micro Devices

Asus A7M266-D Motherboard, AMD Athlon (TM) MP 2400+

SPECfp_rate2000 = 11.2

SPECfp_rate_base2000 = 10.5

SPEC license #: 49 | Tested by: AMD Austin, TX | Test date: Nov-2002 | Hardware Avail: Dec-2002 | Software Avail: Feb-2002

				Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
24	21	18	15	12	9	6	3			
				168.wupwise	2	210	17.7	2	211	17.6
				171.swim	2	668	10.8	2	668	10.8
				172.mgrid	2	562	7.43	2	564	7.40
				173.applu	2	567	8.60	2	564	8.63
				177.mesa	2	139	23.3	2	140	23.2
				178.galgel	2	758	8.87	2	448	15.0
				179.art	2	1160	5.20	2	1170	5.16
				183.equake	2	282	10.7	2	259	11.7
				187.facerec	2	351	12.6	2	351	12.6
				188.amp	2	624	8.19	2	590	8.65
				189.lucas	2	417	11.1	2	414	11.2
				191.fma3d	2	416	11.7	2	415	11.7
				200.sixtrack	2	222	11.5	2	216	11.8
				301.apsi	2	635	9.50	2	551	11.0

Hardware

CPU: AMD Athlon (TM) MP 2400+
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip
 CPU(s) orderable: 1, 2
 Parallel: No
 Primary Cache: 64KBI + 64KBD on chip
 Secondary Cache: 256KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 2x256MB PC2100 DDR SDRAM CL2
 Disk Subsystem: Seagate ST320414A
 Other Hardware: None

Software

Operating System: Windows 2000 SP2
 Compiler: Intel C++ 5.0.1 build 020125Z
 Intel Fortran 5.0.1 build 020125Z and Compaq Visual Fortran 6.6
 Microsoft Visual Studio 6.0 SP5 (libraries)
 MicroQuill SmartHeap Library 5.0
 File System: FAT32
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
 icl and ifl are the Intel C++ and Fortran compilers
 f90 is the Compaq Fortran compiler
 shlW32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
 Portability:
 178.galgel: -FI -Fe\$@ -link -stack:32000000
 Baseline: C icl -QxK -Qipo +FDO shlW32M.lib
 Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
 Peak tuning:
 168.wupwise: ifl -O3 -QxK -Qwp_ipo +FDO
 171.swim: ifl -O3 -QxK -Qwp_ipo +FDO
 172.mgrid: ifl -O3 -QxK -Qwp_ipo +FDO
 173.applu: ifl -O3 -QxK -Qwp_ipo -Qscalar_rep- -Qauto +FDO
 177.mesa: icl -O3 -QxK -Qwp_ipo -Oa +FDO shlW32M.lib
 178.galgel: f90 -Optimize:5 -fast
 179.art: icl -O3 -QxK -QaxW -Qwp_ipo -Oa +FDO shlW32M.lib
 183.equake: icl -O3 -QxK -Qwp_ipo -Qrcd -Oa +FDO shlW32M.lib



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Notes/Tuning Information (Continued)

```
187.facerec: ifl -O3 -QxK -Qipo +FDO
188.ampp:    icl -O3 -QxK -Qwp_ipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qwp_ipo +FDO shlw32M.lib
191.fma3d:   ifl -O3 -QxK -Qwp_ipo +FDO
200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlw32M.lib LIBC.lib libirc.lib OLDNAMES.lib
```

ONESTEP is used for all base and peak runs

The tested system can be assembled using an ATX case such as the Antec KS-282, a 400W power supply such as the Sparkle FSP400-60GN, and a PCI or AGP video card. The System bus runs at 266MHz