



CFP2000 Result

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Advanced Micro Devices

ASUS A7N8X (REV 1.02) Motherboard, AMD Athlon (TM) XP 2800+

SPECfp_rate2000 = 9.78

SPECfp_rate_base2000 = 9.07

SPEC license #: 49 | Tested by: AMD Sunnyvale, CA | Test date: Sep-2002 | Hardware Avail: Dec-2002 | Software Avail: Sep-2002

		Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
16	14	12	10	8	6	4	2	
.....								
		168.wupwise	1	140	13.3	1	140	13.3
		171.swim	1	249	14.5	1	249	14.5
		172.mgrid	1	224	9.34	1	224	9.34
		173.applu	1	300	8.13	1	296	8.22
		177.mesa	1	116	14.0	1	113	14.3
		178.galgel	1	407	8.27	1	235	14.3
		179.art	1	473	6.37	1	459	6.57
		183.quake	1	180	8.39	1	148	10.2
		187.facerec	1	213	10.4	1	213	10.4
		188.amp	1	409	6.23	1	381	6.70
		189.lucas	1	242	9.59	1	241	9.63
		191.fma3d	1	242	10.0	1	243	10.0
		200.sixtrack	1	193	6.60	1	188	6.79
		301.apsi	1	437	6.91	1	381	7.92

Hardware

CPU: AMD Athlon (TM) XP 2800+
 CPU MHz: 2250
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1
 Parallel: No
 Primary Cache: 64KBI + 64KBD on chip
 Secondary Cache: 256KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 2x256MB PC2700 DDR SDRAM CL2.5
 Disk Subsystem: Western Digital WD1200
 Other Hardware: None

Software

Operating System: Windows XP Pro, SP 1
 Compiler: Intel C++ 5.0.1 build 020125Z
 Intel Fortran 5.0.1 build 020125Z
 Compaq Visual Fortran 6.6
 Microsoft Visual Studio 6.0 SP5 (libraries)
 MicroQuill SmartHeap Library 5.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
 icl and ifl are the Intel C++ and Fortran compilers
 f90 is the Compaq Fortran compiler
 shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
 Portability:
 178.galgel: -FI -Fe\$@ -link -stack:32000000
 Baseline: C icl -QxK -Qipo +FDO shlw32M.lib
 Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
 Peak tuning:
 168.wupwise: ifl -O3 -QxK -Qwp_ipo +FDO
 171.swim: ifl -O3 -QxK -Qwp_ipo +FDO
 172.mgrid: ifl -O3 -QxK -Qwp_ipo +FDO
 173.applu: ifl -O3 -QxK -Qwp_ipo -Qscalar_rep- -Qauto +FDO
 177.mesa: icl -O3 -QxK -Qwp_ipo -Oa +FDO shlw32M.lib
 178.galgel: f90 -Optimize:5 -fast
 179.art: icl -O3 -QxK -QaxW -Qwp_ipo -Oa +FDO shlw32M.lib
 183.quake: icl -O3 -QxK -Qwp_ipo -Qrcd -Oa +FDO shlw32M.lib



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Notes/Tuning Information (Continued)

```
187.facerec: ifl -O3 -QxK -Qipo +FDO
188.ammf:    icl -O3 -QxK -Qwp_ipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qwp_ipo +FDO shlw32M.lib
191.fma3d:   ifl -O3 -QxK -Qwp_ipo +FDO
200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlw32M.lib LIBC.lib libirc.lib OLDNAMES.lib
```

ONESTEP is used for all base and peak runs

The tested system can be assembled using an ATX case such as the Antec KS-282, a 300W power supply such as the Sparkle FSP300-60GT, and a PCI or AGP video card. The System bus runs at 333MHz. The two SDRAMs were used in a dual-channel configuration.

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