



CINT2000 Result

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Advanced Micro Devices

ASUS A7N8X (REV 1.02) Motherboard, AMD Athlon (TM) XP 2800+

SPECint2000 = 933

SPECint_base2000 = 898

SPEC license #: 49 Tested by: AMD Sunnyvale, CA Test date: Sep-2002 Hardware Avail: Dec-2002 Software Avail: Sep-2002

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	124	1127	123	1136	
175.vpr	1400	268	522	248	565	
176.gcc	1100	173	635	147	750	
181.mcf	1800	449	401	449	401	
186.crafty	1000	76.4	1310	76.3	1311	
197.parser	1800	221	815	220	817	
252.eon	1300	71.3	1822	64.5	2015	
253.perlbnk	1800	132	1360	132	1360	
254.gap	1100	103	1064	103	1064	
255.vortex	1900	134	1417	129	1473	
256.bzip2	1500	210	715	200	751	
300.twolf	3000	466	644	458	655	

Hardware

CPU: AMD Athlon (TM) XP 2800+
 CPU MHz: 2250
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1
 Parallel: No
 Primary Cache: 64KBI + 64KBD on chip
 Secondary Cache: 256KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 2x256MB PC2700 DDR SDRAM CL2.5
 Disk Subsystem: Western Digital WD1200
 Other Hardware: None

Software

Operating System: Windows XP Pro, SP 1
 Compiler: Intel C++ 5.0.1 build 020125Z
 Microsoft Visual Studio 6.0 SP5 (libraries)
 MicroQuill SmartHeap Library 5.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C: -QxK -Qipo +FDO shlw32M.lib

Baseline C++: -QxK -Qipo -GX -GR

Peak tuning:

164.gzip: -O3 -QxK -Qwp_ipo -Oi- -Oa +FDO

175.vpr: -O3 -Qxi -QaxK -Qwp_ipo +FDO

176.gcc: -O3 -QxK -Qwp_ipo -Oi- +FDO shlw32M.lib

181.mcf: -O3 -QxK -Qwp_ipo +FDO shlw32M.lib

186.crafty: -O3 -QxK -Qwp_ipo -Oa +FDO shlw32M.lib

197.parser: -O3 -QxK -Qwp_ipo +FDO

252.eon: -O3 -QxK -Qwp_ipo +FDO

253.perlbnk: -O3 -QxK -Qipo +FDO shlw32M.lib

254.gap: -O3 -QxK -Qipo -Oi- +FDO

255.vortex: -O3 -QxK -Qwp_ipo +FDO

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Notes/Tuning Information (Continued)

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256.bzip2:    -O3 -QxK -Oa -Qwp_ipo
300.twolf:    -O3 -QxK -Qipo -Oa +FDO shlw32M.lib
ONESTEP is used for all base and peak runs
```

The tested system can be assembled using an ATX case such as the Antec KS-282, a 300W power supply such as the Sparkle FSP300-60GT, and a PCI or AGP video card. The System bus runs at 333MHz. The two SDRAMs were used in a dual-channel configuration.